



Product Specification

G238HAN02.0

AU OPTRONICS CORPORATION

() Preliminary Specification

(V) Final Specification

Module	23.8" Color TFT-LCD
Model Name	G238HAN02.0 with Borderless Design

<table><tr><td>Customer</td><td>Date</td></tr><tr><td> </td><td> </td></tr><tr><td>Checked & Approved by</td><td>Date</td></tr><tr><td> </td><td> </td></tr></table>	Customer	Date			Checked & Approved by	Date			<table><tr><td>Approved by</td><td>Date</td></tr><tr><td><u>Jason CL Pan</u></td><td><u>2020.1.21</u></td></tr><tr><td>Prepared by</td><td>Date</td></tr><tr><td><u>Daniel Chang</u></td><td><u>2020.1.21</u></td></tr></table>	Approved by	Date	<u>Jason CL Pan</u>	<u>2020.1.21</u>	Prepared by	Date	<u>Daniel Chang</u>	<u>2020.1.21</u>
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Record of Revision

Version	Date	Page	Old description	New Description
V0	2018/11/20	All	First draft specification	
V 0.1	2019/02/20	P5	2.1 Power Consumption Power Consumption ^① (LCD Module + Backlight unit) ^② [Watt] ^③ 15.6 (Typ.) ^④	2.1 Power Consumption Power Consumption ^① (LCD Module + Backlight unit) ^② [Watt] ^③ 17.0 (Typ.) ^④ LCD module:PDD (Typ.)=4.6 @ White pattern,Fv=60Hz Backlight unit : P _{BLU} (Typ.) =12.4 @I _{fs} =50mA ^⑤
	2019/02/20	P7	Color Coordinates ^① (CIE 1931) ^② Red x ^③ 0.610 ^④ 0.640 ^⑤ 0.670 ^⑥ Red y ^③ 0.300 ^④ 0.330 ^⑤ 0.360 ^⑥ Green x ^③ 0.270 ^④ 0.300 ^⑤ 0.330 ^⑥ Green y ^③ 0.570 ^④ 0.600 ^⑤ 0.630 ^⑥ Blue x ^③ 0.120 ^④ 0.150 ^⑤ 0.180 ^⑥ Blue y ^③ 0.030 ^④ 0.060 ^⑤ 0.090 ^⑥ White x ^③ 0.255 ^④ 0.285 ^⑤ 0.315 ^⑥ White y ^③ 0.272 ^④ 0.302 ^⑤ 0.332 ^⑥	Color Coordinates ^① (CIE 1931) ^② Red x ^③ 0.597 ^④ 0.627 ^⑤ 0.657 ^⑥ Red y ^③ 0.300 ^④ 0.330 ^⑤ 0.360 ^⑥ Green x ^③ 0.268 ^④ 0.298 ^⑤ 0.328 ^⑥ Green y ^③ 0.563 ^④ 0.593 ^⑤ 0.623 ^⑥ Blue x ^③ 0.124 ^④ 0.154 ^⑤ 0.184 ^⑥ Blue y ^③ 0.027 ^④ 0.057 ^⑤ 0.087 ^⑥ White x ^③ 0.255 ^④ 0.285 ^⑤ 0.315 ^⑥ White y ^③ 0.272 ^④ 0.302 ^⑤ 0.332 ^⑥
		P13	LCM back drawing : Non sensor hole	LCM back drawing : Add sensor hole
		P14	3.3.2 Recommended Operating Condition IDD ^① Power supply ^② Input Current (RMS) ^③ ~ ^④ 0.64 ^⑤ 0.76 ^⑥ ~ ^④ 0.74 ^⑤ 0.88 ^⑥ PDD ^① VDD Power Consumption ^② ~ ^④ 3.2 ^⑤ 3.8 ^⑥ ~ ^④ 3.7 ^⑤ 4.4 ^⑥	3.3.2 Recommended Operating Condition IDD ^① Power supply ^② Input Current (RMS) ^③ ~ ^④ 0.92 ^⑤ 1.10 ^⑥ ~ ^④ 1.05 ^⑤ 1.26 ^⑥ PDD ^① VDD Power Consumption ^② ~ ^④ 4.6 ^⑤ 5.5 ^⑥ ~ ^④ 5.25 ^⑤ 6.3 ^⑥
		P 28, P29	7 Preliminary Mechanical Characteristics Non drawing	7 Preliminary Mechanical Characteristics Drawing added
		P30	Non Packing rule	Add Packing SOP
V0.2	2019/11/19	P5	2.1 Display Characteristics Outline Dimension ^① [mm] ^② 534.74(H)x313.66(V)x11.85(D)(Typ.) ^③	2.1 Display Characteristics Outline Dimension ^① [mm] ^② 534.74(H)x313.66(V)x13.3(D)(Typ.) ^③
		P28	*7 Preliminary Mechanical Characteristics ^① 	*7 Preliminary Mechanical Characteristics ^① 
V1.0	2020/01/21	P1	Preliminary Specification	Final Specification

1. Operating Precautions

- 1) Since front polarizer is easily damaged, please be cautious and not to scratch it.
- 2) Be sure to turn off power supply when inserting or disconnecting from input connector.
- 3) Wipe off water drop immediately. Long contact with water may cause discoloration or spots.
- 4) When the panel surface is soiled, wipe it with absorbent cotton or soft cloth.
- 5) Since the panel is made of glass, it may be broken or cracked if dropped or bumped on hard surface.
- 6) To avoid ESD (Electro Static Discharge) damage, be sure to ground yourself before handling TFT-LCD Module.
- 7) Do not open nor modify the module assembly.
- 8) Do not press the reflector sheet at the back of the module to any direction.
- 9) In case if a module has to be put back into the packing container slot after it was taken out from the container, do not press the center of the LED light bar edge. Instead, press at the far ends of the LED light bar edge softly. Otherwise the TFT Module may be damaged.
- 10) At the insertion or removal of the Signal Interface Connector, be sure not to rotate nor tilt the Interface Connector of the TFT Module.
- 11) TFT-LCD Module is not allowed to be twisted & bent even force is added on module in a very short time. Please design your display product well to avoid external force applying to module by end-user directly.
- 12) Small amount of materials having no flammability grade is used in the LCD module. The LCD module should be supplied by power complied with requirements of Limited Power Source (IEC60950 or UL1950), or be applied exemption.
- 13) Severe temperature condition may result in different luminance, response time and lamp ignition voltage.
- 14) Continuous operating TFT-LCD display under low temperature environment may accelerate lamp exhaustion and reduce luminance dramatically.
- 15) The data on this specification sheet is applicable when LCD module is placed in landscape position.
- 16) Continuous displaying fixed pattern may induce image sticking. It's recommended to use screen saver or shuffle content periodically if fixed pattern is displayed on the screen.

2. General Description

This specification applies to the Color Active Matrix Liquid Crystal Display G238HAN02.0 composed of a TFT-LCD display, a driver and power supply circuit, and a LED backlight system. The screen format is intended to support the FHD (1928(H)x1080(V)) screen and 16.7M colors. All input signals are 2-channel LVDS interface compatible. LED driving board for backlight unit is excluded in G238HAN02.0

2.1 Display Characteristics

The following items are characteristics summary on the table under 25 °C condition:

ITEMS	Unit	SPECIFICATIONS
Screen Diagonal	[mm]	604.70 (23.81")
Active Area	[mm]	527.04(H) x 296.46(V)
Pixels H x V	-	1920(x3) x 1080
Pixel Pitch	[um]	274.5 (per one triad) x274.5
Pixel Arrangement	-	R.G.B. Vertical Stripe
Display Mode	-	AHVA, normally Black
White Luminance (Center)	[cd/m ²]	300 (Typ.)
Contrast Ratio	-	1000:1 (Typ.)
Response Time	[msec]	14 (G to G)
Power Consumption (LCD Module + Backligh unit)	[Watt]	17.0 (Typ.) LCD module:PDD (Typ.)=4.6 @ White pattern,Fv=60Hz Backlight unit : P _{BLU} (Typ.) =12.4 @Is=50mA
Weight	[Grams]	2200 (typ.)
Outline Dimension	[mm]	534.74(H) x 313.66(V) x 13.3(D) (Typ.)
Electrical Interface	-	Dual channel LVDS
Support Color	-	16.7M colors (RGB 8-bit)
Surface Treatment	-	Anti-Glare, 3H
Temperature Range Operating Storage (Shipping)	[°C] [°C]	0 to +50 -20 to +60

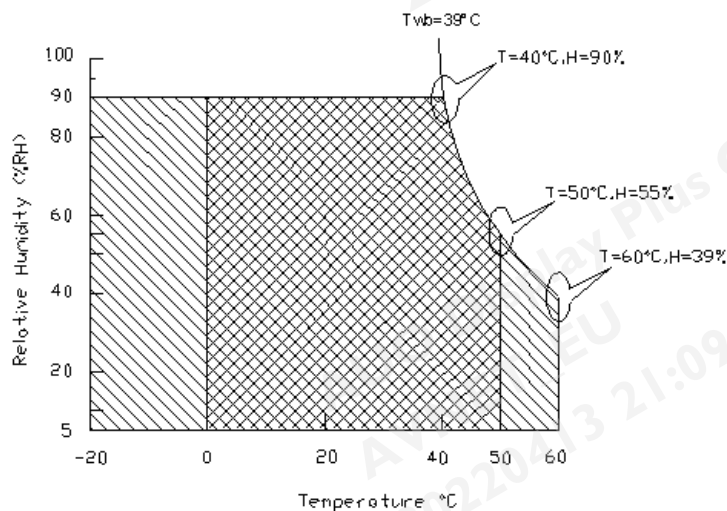
2.2 Absolute Maximum Rating of Environment

Permanent damage may occur if exceeding the following maximum rating.

Symbol	Description	Min.	Max.	Unit	Remark
TOP	Operating Temperature	0	+50	[°C]	Note 2-1
TGS	Glass surface temperature (operation)	0	+65	[°C]	Note 2-1 Function judged only
HOP	Operation Humidity	5	90	[%RH]	Note 2-1
TST	Storage Temperature	-20	+60	[°C]	
HST	Storage Humidity	5	90	[%RH]	

Note 2-1: Temperature and relative humidity range are shown as the below figure.

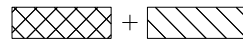
1. 90% RH Max ($T_a \leq 39^\circ\text{C}$)
2. Max wet-bulb temperature at 39°C or less. ($T_a \leq 39^\circ\text{C}$)
3. No condensation



Operating Range



Storage Range



2.3 Optical Characteristics

The optical characteristics are measured on the following test condition.

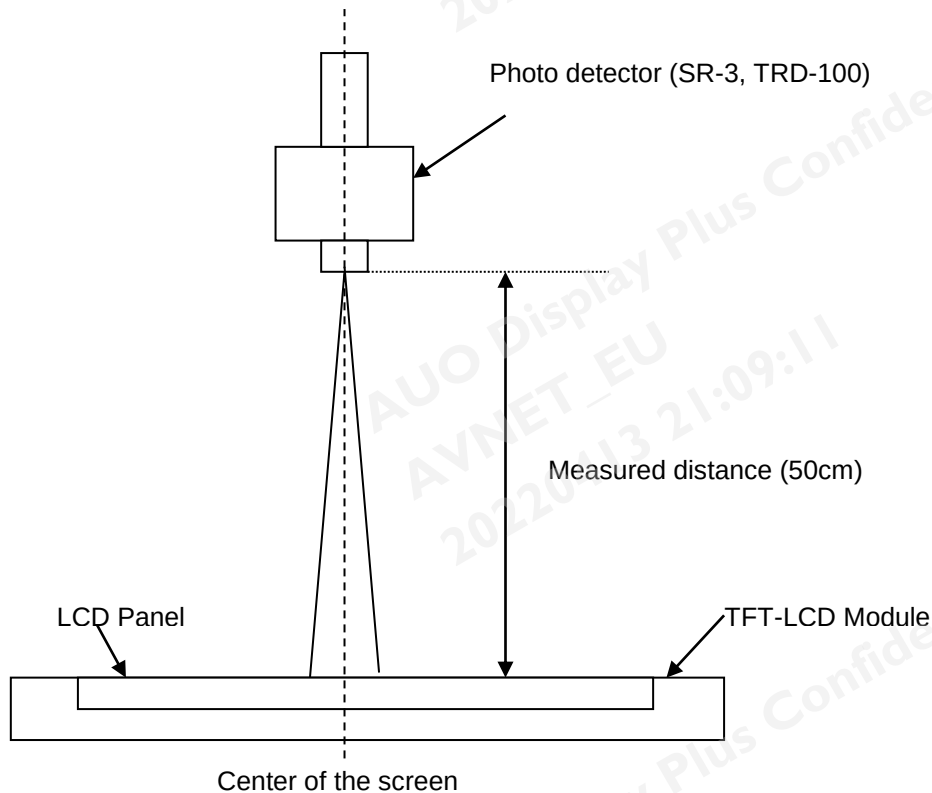
Test Condition:

1. Equipment setup: Please refer to **Note 2-2**.
2. Panel Lighting time: 30 minutes
3. VDD=5.0V, Fv=60Hz, Is=50mA, Ta=25°C

Symbol	Description		Min.	Typ.	Max.	Unit	Remark
L _w	White Luminance (Center of screen)		240	300	-	[cd/m ²]	Note 2-2 By SR-3
L _{uni}	Luminance Uniformity (9 points)		75	80	-	[%]	Note 2-3 By SR-3
CR	Contrast Ratio (Center of screen)		700	1000	-	-	Note 2-4 By SR-3
θ _R	Horizontal Viewing Angle (CR=10)	Right	75	89	-	[degree]	Note 2-5 By SR-3
θ _L		Left	75	89	-		
Φ _H	Vertical Viewing Angle (CR=10)	Up	75	89	-		
Φ _L		Down	75	89	-		
TGTG	Response Time	Gray to Gray	-	14	-	[msec]	Note 2-6 By TRD-100
R _x	Color Coordinates (CIE 1931)	Red x	0.597	0.627	0.657	-	By SR-3
R _y		Red y	0.300	0.330	0.360		
G _x		Green x	0.268	0.298	0.328		
G _y		Green y	0.563	0.593	0.623		
B _x		Blue x	0.124	0.154	0.184		
B _y		Blue y	0.027	0.057	0.087		
W _x		White x	0.255	0.285	0.315		
W _y		White y	0.272	0.302	0.332		
CT	Crosstalk		-	-	1.5	[%]	Note 2-7 By SR-3
F _{dB}	Flicker (Center of screen)		-	-	-20	[dB]	Note 2-8 By SR-3

Note 2-2: Equipment setup :

The LCD module should be stabilized at given temperature for 30 minutes to avoid abrupt temperature change during measuring (at surface 35°C). In order to stabilize the luminance, the measurement should be executed after lighting Backlight for 30 minutes in a stable, windless and dark room.

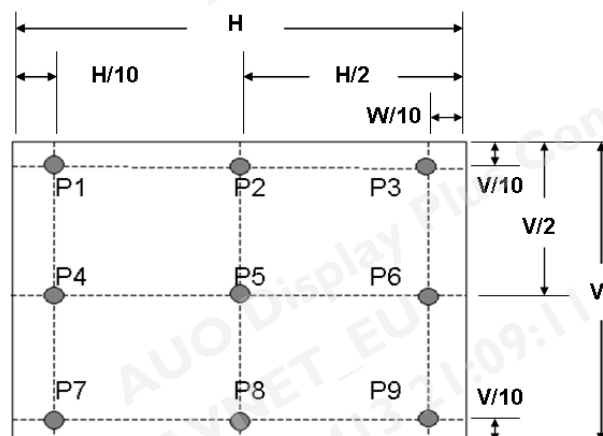


Note 2-3: Luminance Uniformity Measurement

Definition:

$$\text{Luminance Uniformity} = \frac{\text{Minimum Luminance of 9 Points (P1 ~ P9)}}{\text{Maximum Luminance of 9 Points (P1 ~ P9)}}$$

Test pattern: White Pattern



Note 2-4: Contrast Ratio Measurement

Definition:

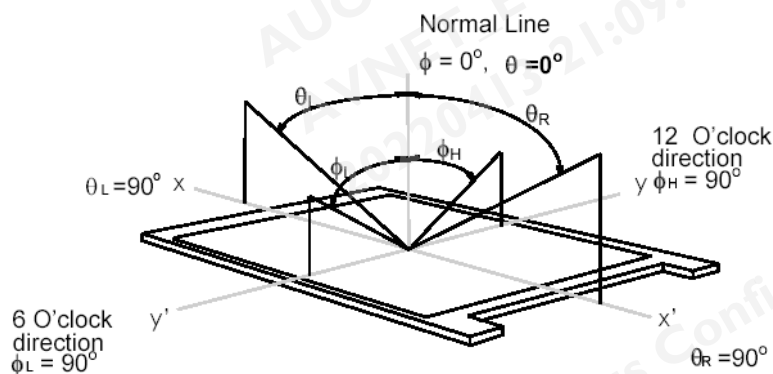
$$\text{Contrast Ratio} = \frac{\text{Luminance of White pattern}}{\text{Luminance of Black pattern}}$$

- a. Measured position: Center of screen (P5) & perpendicular to the screen ($\theta = \Phi = 0^\circ$)

Note 2-5: Viewing angle measurement

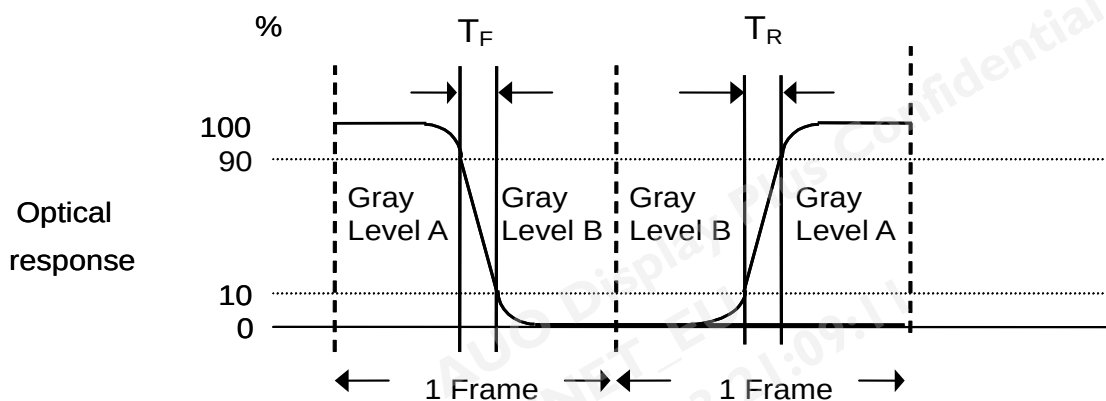
Definition: The angle at which the contrast ratio is greater than 10

- a. Horizontal view angle: Divide to left & right (θ_L & θ_R)
Vertical view angle: Divide to up & down (Φ_H & Φ_L)



Note 2-6: Response time measurement

The output signals of photo detector are measured when the input signals are changed from "Gray level A" to "Gray level B" (falling time, T_F), and from "Gray level B" to "Gray level A" (rising time, T_R), respectively. The response time is interval between the 10% and 90% of optical response.



The gray to gray response time is defined as the following table.

Gray Level to Gray Level		Falling Time				
		G0	G63	G127	G191	G255
Rising Time	G0					
	G63					
	G127					
	G191					
	G255					

■ T_{GTG_typ} is the total average time at rising time and falling time of gray to gray.

Note 2-7: Crosstalk measurement

Definition:

$$CT = \text{Max. } (CT_H, CT_V);$$

Where

- a. Maximum Horizontal Crosstalk :

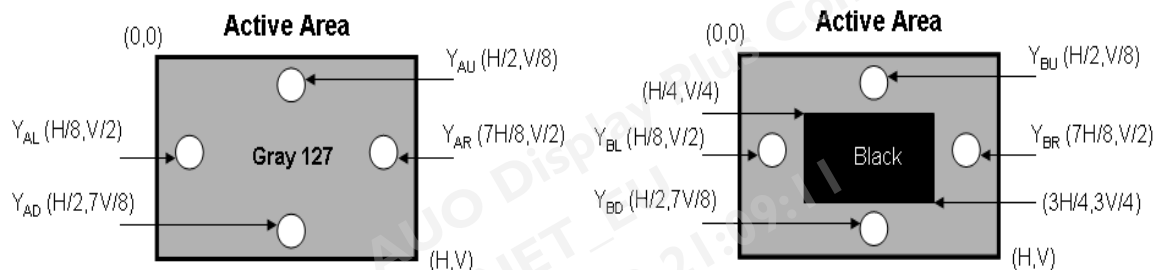
$$CT_H = \text{Max. } (|Y_{BL} - Y_{AL}| / Y_{AL} \times 100 \%, |Y_{BR} - Y_{AR}| / Y_{AR} \times 100 \%);$$

Maximum Vertical Crosstalk:

$$CT_V = \text{Max. } (|Y_{BU} - Y_{AU}| / Y_{AU} \times 100 \%, |Y_{BD} - Y_{AD}| / Y_{AD} \times 100 \%);$$

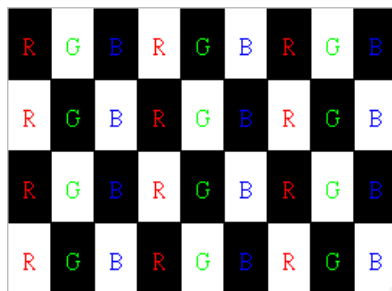
- b. $Y_{AU}, Y_{AD}, Y_{AL}, Y_{AR}$ = Luminance of measured location without Black pattern

$Y_{BU}, Y_{BD}, Y_{BL}, Y_{BR}$ = Luminance of measured location with Black pattern



Note 2-8: Flicker measurement

- a. Test pattern: It is listed as following.



R: Red, G: Green, B:Blue



Gray level = L0



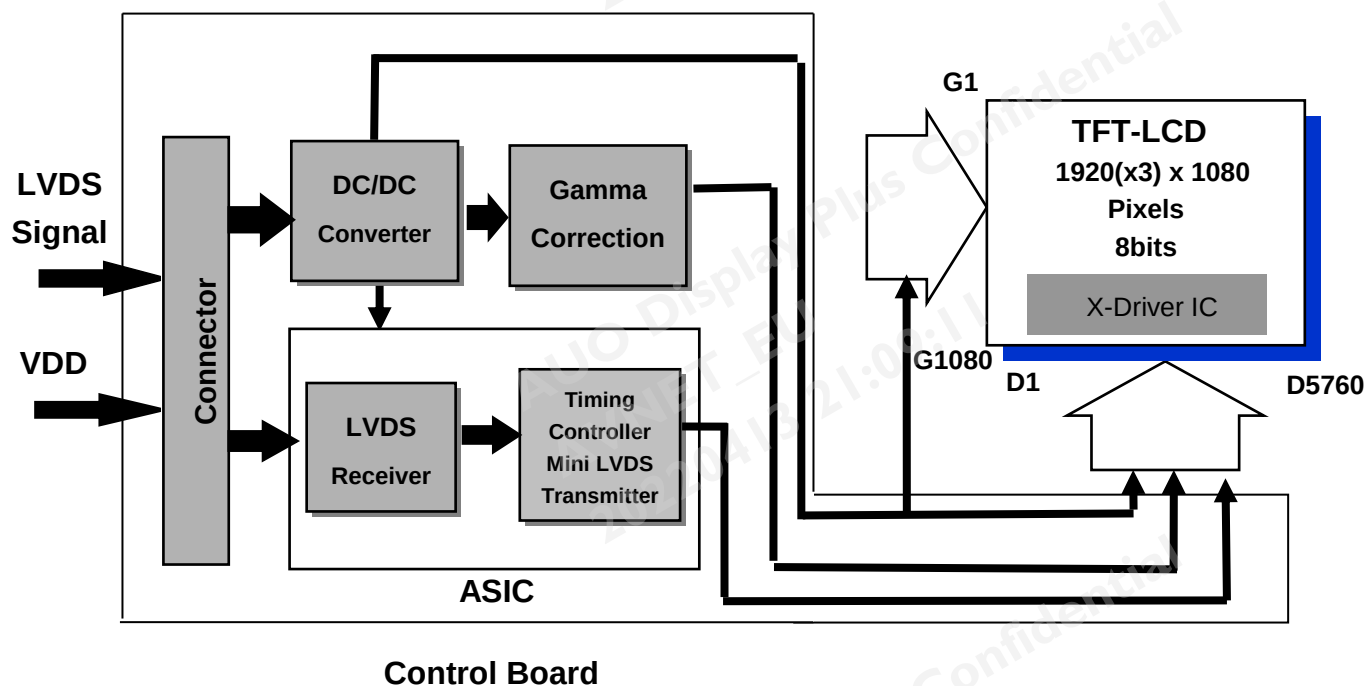
Gray level = L127

- b. Measured position: Center of screen (P5) & perpendicular to the screen ($\theta = \Phi = 0^\circ$)

3 TFT-LCD Module

3.1 Block Diagram

The following shows the block diagram of the 23.8 inch Color TFT-LCD Module.



3.2 Interface Connection

3.2.1 Connector Type

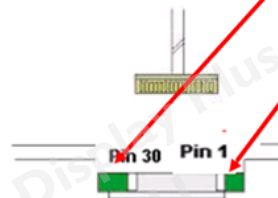
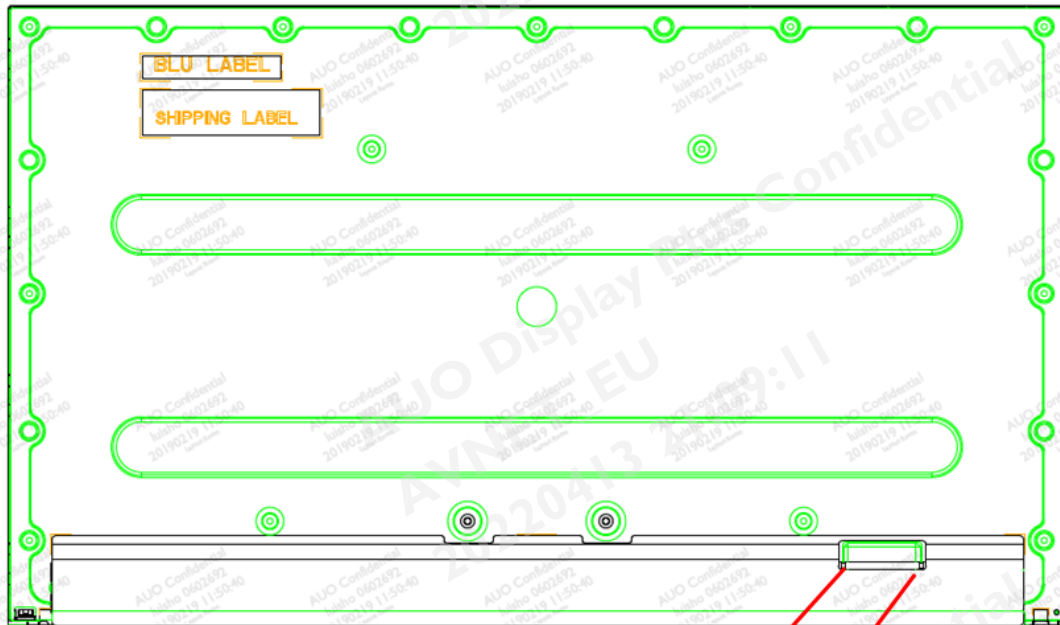
TFT-LCD Connector	Manufacturer	P-TWO	STM
	Part Number	187034-3009	MSBKT2407P30HB
Mating Connector	Manufacturer	JAE or Equivalent	
	Part Number	FI-X30HL (Locked Type)	

3.2.2 Connector Pin Assignment

PIN #	Symbol	Description	Remark
1	RxO0-	Negative LVDS differential data input (Odd data)	
2	RxO0+	Positive LVDS differential data input (Odd data)	
3	RxO1-	Negative LVDS differential data input (Odd data)	
4	RxO1+	Positive LVDS differential data input (Odd data)	
5	RxO2-	Negative LVDS differential data input (Odd data)	
6	RxO2+	Positive LVDS differential data input (Odd data)	
7	GND	Ground	
8	RxOCLK-	Negative LVDS differential clock input (Odd clock)	
9	RxOCLK+	Positive LVDS differential clock input (Odd clock)	
10	RxO3-	Negative LVDS differential data input (Odd data)	
11	RxO3+	Positive LVDS differential data input (Odd data)	
12	RxE0-	Negative LVDS differential data input (Even data)	
13	RxE0+	Positive LVDS differential data input (Even data)	
14	GND	Ground	
15	RxE1-	Negative LVDS differential data input (Even data)	
16	RxE1+	Positive LVDS differential data input (Even data)	
17	GND	Ground	
18	RxE2-	Negative LVDS differential data input (Even data)	
19	RxE2+	Positive LVDS differential data input (Even data)	
20	RxECLK-	Negative LVDS differential clock input (Even clock)	
21	RxECLK+	Positive LVDS differential clock input (Even clock)	
22	RxE3-	Negative LVDS differential data input (Even data)	
23	RxE3+	Positive LVDS differential data input (Even data)	
24	GND	Must Connect to GND	
25	NC	No connection (for AUO test only. Do not connect)	
26	NC	No connection (for AUO test only. Do not connect)	
27	NC	No connection (for AUO test only. Do not connect)	
28	VDD	Power Supply Input Voltage	

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29	VDD	Power Supply Input Voltage	
30	VDD	Power Supply Input Voltage	



3.3 Electrical Characteristics

3.3.1 Absolute Maximum Rating

Permanent damage may occur if exceeding the following maximum rating.

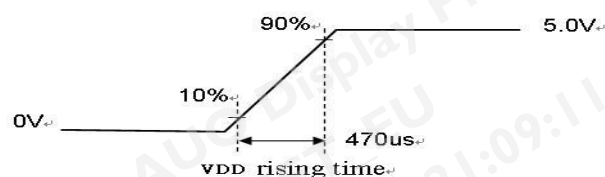
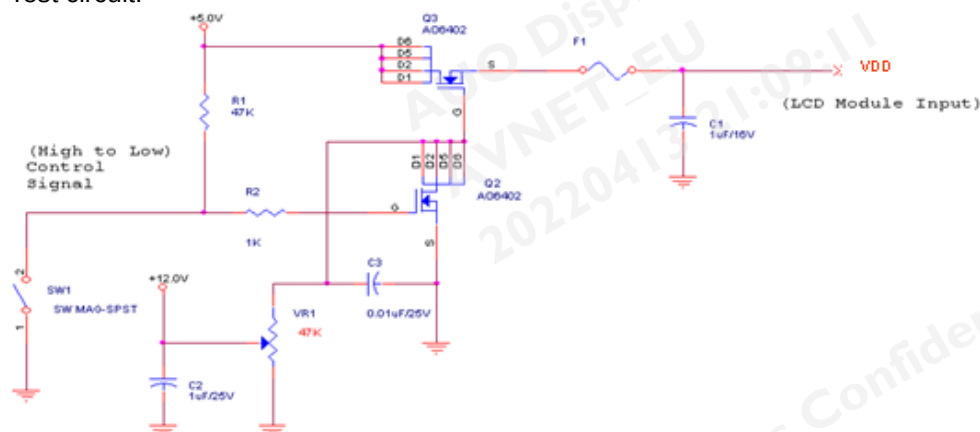
Symbol	Description	Min	Max	Unit	Remark
VDD	Power Supply Input Voltage	GND-0.3	6.0	[Volt]	Ta=25°C

3.3.2 Recommended Operating Condition

Symbol	Description	Min	Typ	Max	Unit	Remark
VDD	Power supply Input voltage	4.5	5.0	5.5	[Volt]	
IDD	Power supply Input Current (RMS)	-	0.92	1.10	[A]	VDD= 5.0V, White Pattern, Fv=60Hz
		-	1.05	1.26	[A]	VDD= 5.0V, White Pattern, Fv=75Hz
PDD	VDD Power Consumption	-	4.6	5.5	[Watt]	VDD= 5.0V, White Pattern, Fv=60Hz
		-	5.25	6.3	[Watt]	VDD= 5.0V, White Pattern, Fv=75Hz
IRush	Inrush Current	-	-	3.0	[A]	Note 3-1
VDDrp	Allowable VDD Ripple Voltage	-	-	500	[mV]	VDD= 5.0V, Black Pattern, Fv=75Hz

Note 3-1: Inrush Current measurement:

Test circuit:



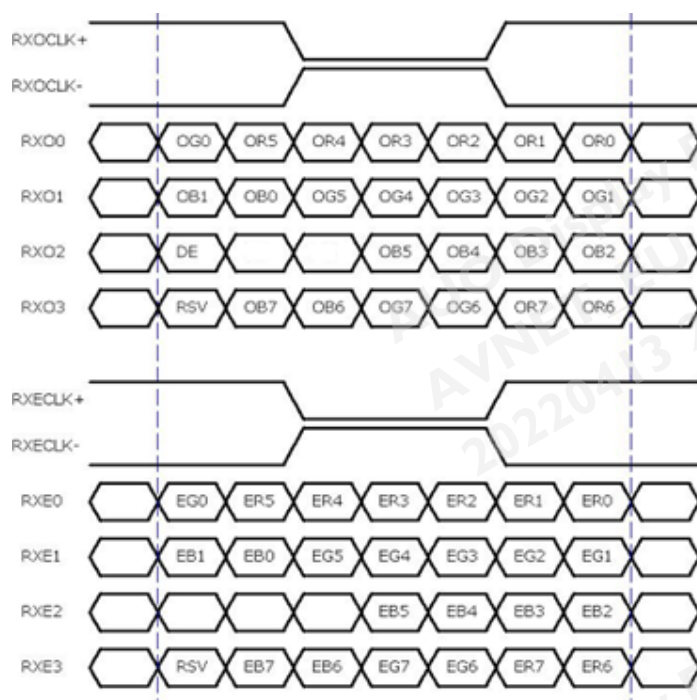
The duration of VDD rising time: 470us.

3.4 Signal Characteristics

3.4.1 LCD Pixel Format

	1			2									1919			1920		
1st Line	R	G	B	R	G	B							R	G	B	R	G	B
	.	.	.	.	.	.	.						.	.	.	.	.	.
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1080 Line	R	G	B	R	G	B							R	G	B	R	G	B

3.4.2 LVDS Data Format



8 Bit Color Bit Order			
MSB	R7	G7	B7
	R6	G6	B6
	R5	G5	B5
	R4	G4	B4
	R3	G3	B3
	R2	G2	B2
	R1	G1	B1
LSB	R0	G0	B0

Note 3-2:

- a. O = "Odd Pixel Data" E = "Even Pixel Data"
- b. Refer to 3.4.1 LCD pixel format, the 1st data is 1 (Odd Pixel Data), the 2nd data is 2 (Even Pixel Data) and the last data is 1920 (Even Pixel Data).

3.4.3 Color versus Input Data

The following table is for color versus input data (8bit). The higher the gray level, the brighter the color.

Color	Gray Level	Color Input Data																								Remark
		RED data (MSB:R7, LSB:R0)								GREEN data (MSB:G7, LSB:G0)								BLUE data (MSB:B7, LSB:B0)								
		R7	R6	R5	R4	R3	R2	R1	R0	G7	G6	G5	G4	G3	G2	G1	G0	B7	B6	B5	B4	B3	B2	B1	B0	
Black	-	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0		
White	-	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	
Gray 127	-	0	1	1	1	1	1	1	1	0	1	1	1	1	1	1	1	0	1	1	1	1	1	1	1	
Red	L0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	Black
	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	
	L255	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
Green	L0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	Black
	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	
	L255	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	
Blue	L0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	Black
	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	
	L255	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	

3.4.4 LVDS Specification

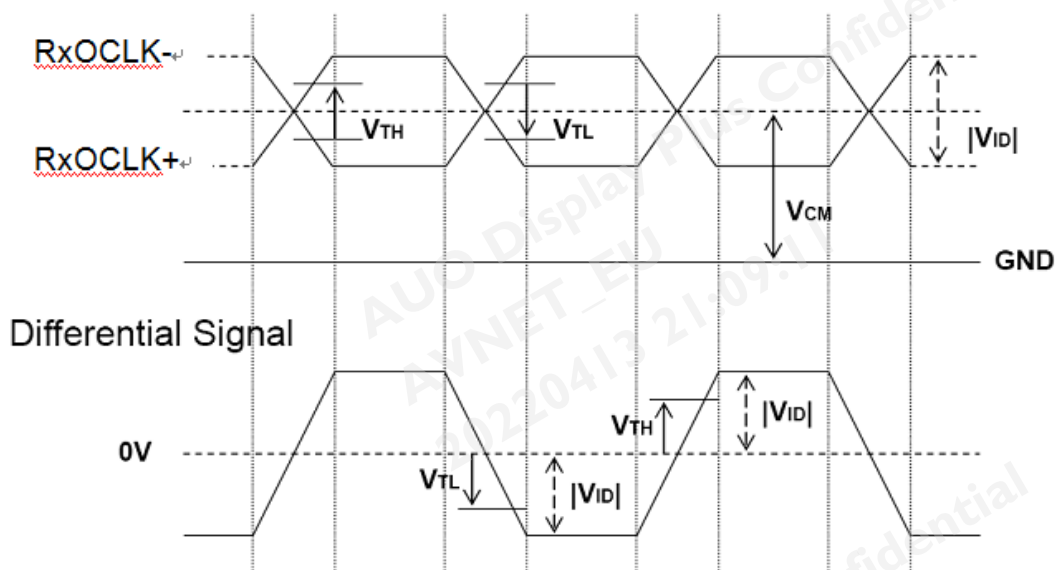
a. DC Characteristics:

Symbol	Description	Min	Typ	Max	Units	Condition
V_{TH}	LVDS Differential Input High Threshold	-	-	+100	[mV]	$V_{CM} = 1.2V$
V_{TL}	LVDS Differential Input Low Threshold	-100	-	-	[mV]	$V_{CM} = 1.2V$
$ V_{ID} $	LVDS Differential Input Voltage	100	-	600	[mV]	
V_{CM}	LVDS Common Mode Voltage	+1.0	+1.2	+1.5	[V]	$V_{TH} - V_{TL} = 200mV$

LVDS Signal Waveform:

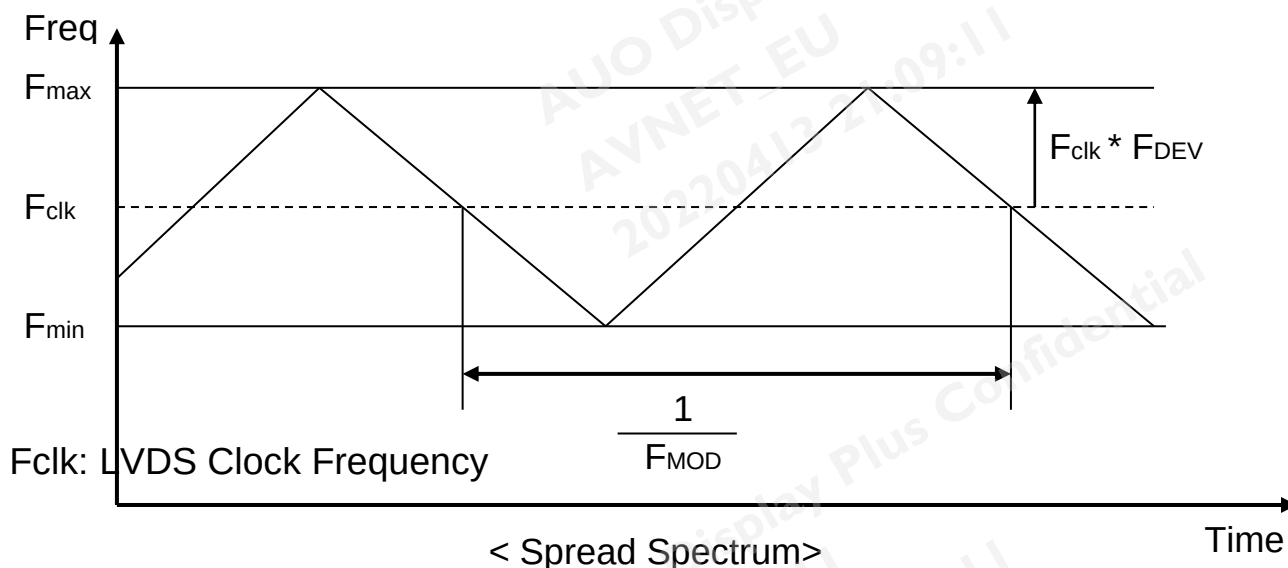
Use RxOCLK- & RxOCLK+ as example

Single-End



b. AC Characteristics:

Symbol	Description	Min	Max	Unit	Remark
F_{DEV}	Maximum deviation of input clock frequency during Spread Spectrum	-	± 3	%	
F_{MOD}	Maximum modulation frequency of input clock during Spread Spectrum	-	200	KHz	



3.4.5 Input Timing Specification

It only support DE mode, and the input timing are shown as the following table.

Symbol	Description		Min.	Typ.	Max.	Unit	Remark
Tv	Vertical Section	Period	1092	1130	1793	Th	
Tdisp (v)		Active	1080	1080	1080	Th	
Tblk (v)		Blanking	12	50	713	Th	
Fv		Frequency	49	60	76	Hz	
Th	Horizontal Section	Period	1004	1050	1100	Tclk	
Tdisp (h)		Active	960	960	960	Tclk	
Tblk (h)		Blanking	44	90	140	Tclk	
Fh		Frequency	54	68	90	KHz	Note 3-3
Tclk	LVDS Clock	Period	11.1	14.0	18.2	ns	1/Fclk
Fclk		Frequency	53.7	71.2	90.0	MHz	Note 3-4

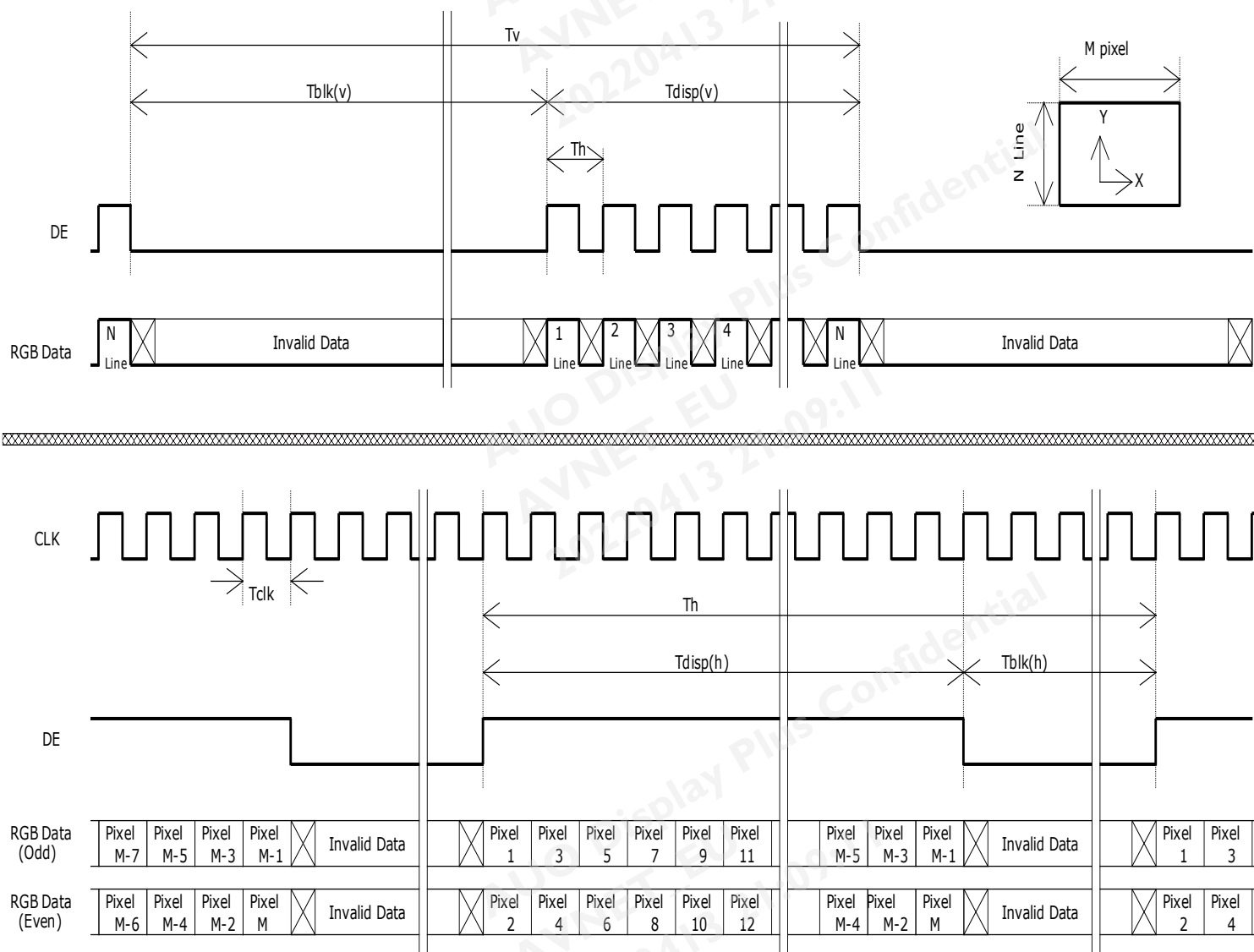
Note 3-3: The equation is listed as following. Please don't exceed the above recommended value.

$$\begin{aligned} Fh (\text{Min.}) &= Fclk (\text{Min.}) / Th (\text{Min.}); \\ Fh (\text{Typ.}) &= Fclk (\text{Typ.}) / Th (\text{Typ.}); \\ Fh (\text{Max.}) &= Fclk (\text{Max.}) / Th (\text{Min.}); \end{aligned}$$

Note 3-4: The equation is listed as following. Please don't exceed the above recommended value.

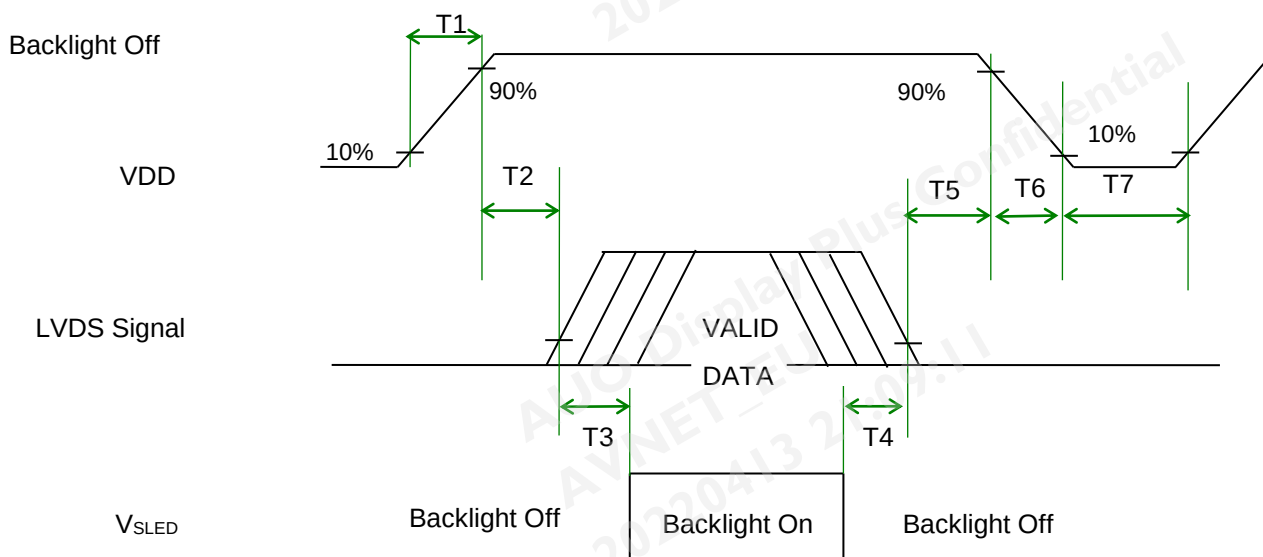
$$\begin{aligned} Fclk (\text{Min.}) &= Fv (\text{Min.}) \times Th (\text{Min.}) \times Tv (\text{Min.}); \\ Fclk (\text{Typ.}) &= Fv (\text{Typ.}) \times Th (\text{Typ.}) \times Tv (\text{Typ.}); \\ Fclk (\text{Max.}) &= Fv (\text{Max.}) \times Th (\text{Typ.}) \times Tv (\text{Typ.}); \end{aligned}$$

3.4.6 Input Timing Diagram



3.5 Power ON/OFF Sequence

VDD power, LVDS signal and backlight on/off sequence are as following. LVDS signals from any system shall be Hi-Z state when VDD is off.



Power Sequence Timing

Symbol	Value			Unit	Remark
	Min.	Typ.	Max.		
T1	0.5	-	10	[ms]	
T2	0	-	50	[ms]	
T3	500	-	-	[ms]	
T4	100	-	-	[ms]	
T5	0	-	50	[ms]	Note 3-5, Note 3-6
T6	0	-	150	[ms]	Note 3-6
T7	1000	-	-	[ms]	

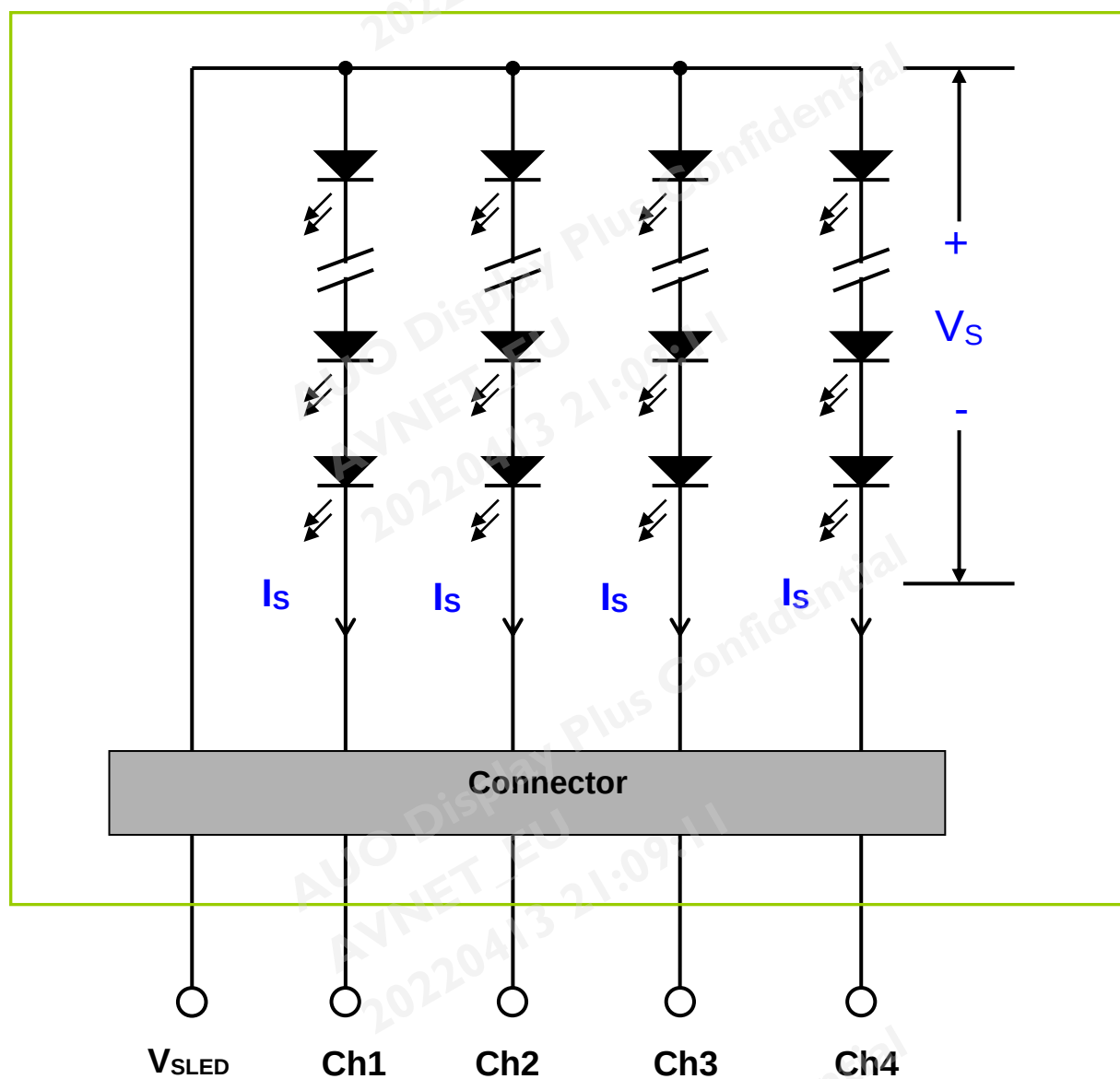
Note 3-5 : Recommend setting T₅ = 0ms to avoid electronic noise when VDD is off.

Note 3-6 : During T₅ and T₆ period , please keep the level of input LVDS signals with Hi-Z state.

4 Backlight Unit

4.1 Block Diagram

The following shows the block diagram of the 23.8 inch Backlight Unit.



4.2 Interface Connection

4.2.1 Connector Type

Backlight Light Bar Connector	Manufacturer	CviLux
	Part Number	CI1406M1VL0-NH

4.2.2 Backlight Light Bar Connector Pin Assignment

Pin no.	Signal name
1	IRLED (current out)
2	IRLED (current out)
3	VLED (voltage in)
4	VLED (voltage in)
5	IRLED (current out)
6	IRLED (current out)

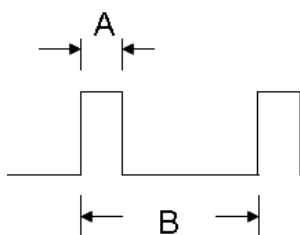
4.3 Electrical Characteristics

4.3.1 Absolute Maximum Rating

Permanent damage may occur if exceeding the following maximum rating.

(Ta=25°C)

Symbol	Description	Min	Max	Unit	Remark
Is	Single LED Current	0	50	[mA]	100% duty ratio
			-	[mA]	Duty ratio ≤ 10% Pulse time=10 ms



Duty ratio= (A / B) X 100% ; (A: Pulse time, B: Period)

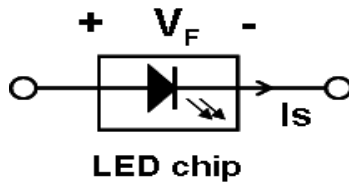
4.3.2 Recommended Operating Condition

(Ta=25°C)

Symbol	Description	Min.	Typ.	Max.	Unit	Remark
Is	LED String Current	-	50	TBD	[mA]	100% duty ratio of LED chip
Vs	LED String Voltage	-	62	68	[Volt]	Is=50mA @ 100% duty ratio; Note 4-1, Note 4-5
ΔVs	Maximum Vs Voltage Deviation of light bar	-	3.1	3.4	[Volt]	Is=50mA @ 100% duty ratio; Note 4-2
P _{BLU}	LED Light Bar Power Consumption	-	12.4	13.6	[Watt]	Note 4-3
LT _{LED}	LED Life Time	50,000	-	-	[Hour]	Note 4-4
OVP	Over Voltage Protection in system board	(110%) Vs (max)	-	-	[Volt]	Note 4-5

Note 4-1: $V_s (\text{Typ.}) = V_F (\text{Typ.}) \times \text{LED No. (one string)}$;

- V_F : LED chip forward voltage, $V_F (\text{Min.})=2.7$, $V_F (\text{Typ.})=3.2\text{V}$, $V_F (\text{Max.})=3.6\text{V}$
- The same equation to calculate $V_s (\text{Min.})$ & $V_s (\text{Max.})$ for respective $V_F (\text{Min.})$ & $V_F (\text{Max.})$;



Note 4-2: $\Delta V_s (\text{Max.}) = \Delta V_F \times \text{LED No. (one string)}$;

- ΔV_F : LED chip forward voltage deviation; (0.2 V , each Bin of LED V_F)

Note 4-3: $P_{BLU} (\text{Typ.}) = V_s (\text{Typ.}) \times I_s (\text{Typ.}) \times 4$; (4 is total String No. of LED Light bar)

$$P_{BLU} (\text{Max.}) = V_s (\text{Max.}) \times I_s (\text{Typ.}) \times 4 ;$$

Note 4-4: Definition of life time:

- Brightness of LED becomes to 50% of its original value
- Test condition: $I_s = 70\text{mA}$ and 25°C (Room Temperature)

Note 4-5: Recommendation for LED driver power design:

Due to there are electrical property deviation in LED & monitor set system component after long time operation. AUO strongly recommend the design value of LED driver board OVP (over voltage protection) should be 10% higher than max. value of LED string voltage (V_s) at least.

Note 4-6: AUO strongly recommend "Analog Dimming" method for backlight brightness control for Wavy Noise Free. Otherwise, recommend that Dimming Control Signal (PWM Signal) should be synchronized with Frame Frequency.

5 Reliability Test

AUO reliability test items are listed as following table. (Bare Panel only)

Items	Condition	Remark
Temperature Humidity Bias (THB)	Ta= 50°C, 80%RH, 300hours	
High Temperature Operation (HTO)	Ta= 50°C, 300hours	
Low Temperature Operation (LTO)	Ta= 0°C, 300hours	
High Temperature Storage (HTS)	Ta= 60°C, 300hours	
Low Temperature Storage (LTS)	Ta= -20°C, 300hours	
Vibration Test (Non-operation)	Acceleration: 1.5 Grms Wave: Random Frequency: 10 - 200 Hz Sweep: 30 Minutes each Axis (X, Y, Z)	
Shock Test (Non-operation)	Acceleration: 50 G Wave: Half-sine Active Time: 20 ms Direction: ±X, ±Y, ±Z (one time for each Axis)	
Thermal Shock Test (TST)	-20°C/30min, 60°C/30min, 100 cycles	Note 1
On/Off Test	On/10sec, Off/10sec, 30,000 cycles	
ESD (Electro Static Discharge)	Contact Discharge: ± 8KV, 150pF(330Ω) 1sec, 8 points, 25 times/ point.	Note 2
	Air Discharge: ± 15KV, 150pF(330Ω) 1sec 8 points, 25 times/ point.	
Altitude Test	Operation:18,000 ft Non-Operation:40,000 ft	

Note 1: A cycle of rapid temperature change consists of varying the temperature from -20°C to 60°C, and back again.

Power is not applied during the test. After finish temperature cycling, the unit is placed in normal room ambient for at least 4 hours before power on.

Note 2: According to EN61000-4-2, ESD class B: Some performance degradation allowed. No data lost
Self-recoverable. No hardware failures.

Note3:

- Water condensation is not allowed for each test items.
- Each test is done by new TFT-LCD module. Don't use the same TFT-LCD module repeatedly for reliability test.
- The reliability test is performed only to examine the TFT-LCD module capability.
- To inspect TFT-LCD module after reliability test, please store it at room temperature and room humidity for 24 hours at least in advance.
- No function failure occurs.

6 Shipping Label

The label is on the panel as shown below:



Note 6-1: For Pb Free products, AUO will add  for identification.

Note 6-2: For RoHS compatible products, AUO will add  for identification.

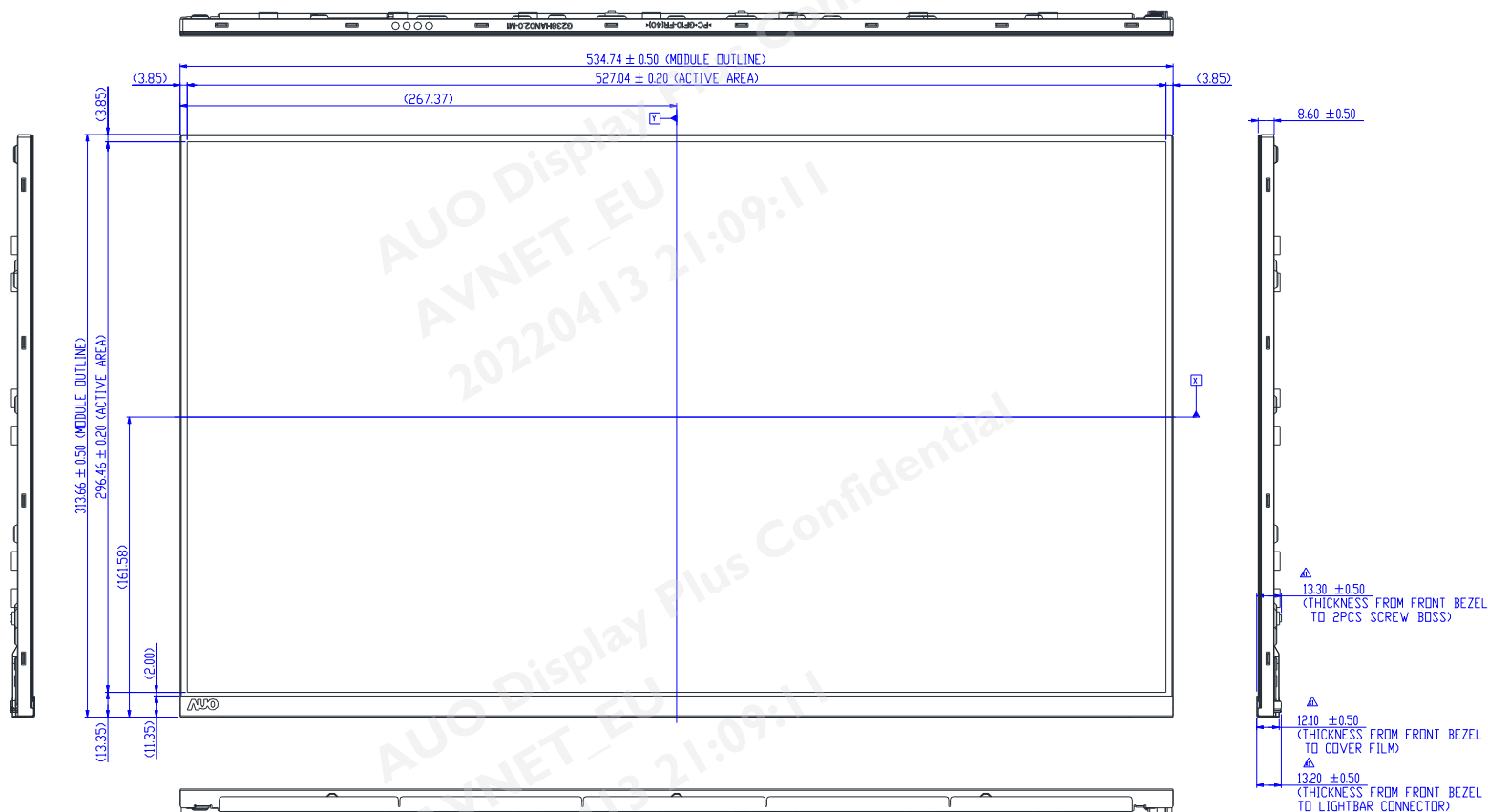
Note 6-3: For China RoHS compatible products, AUO will add  for identification.

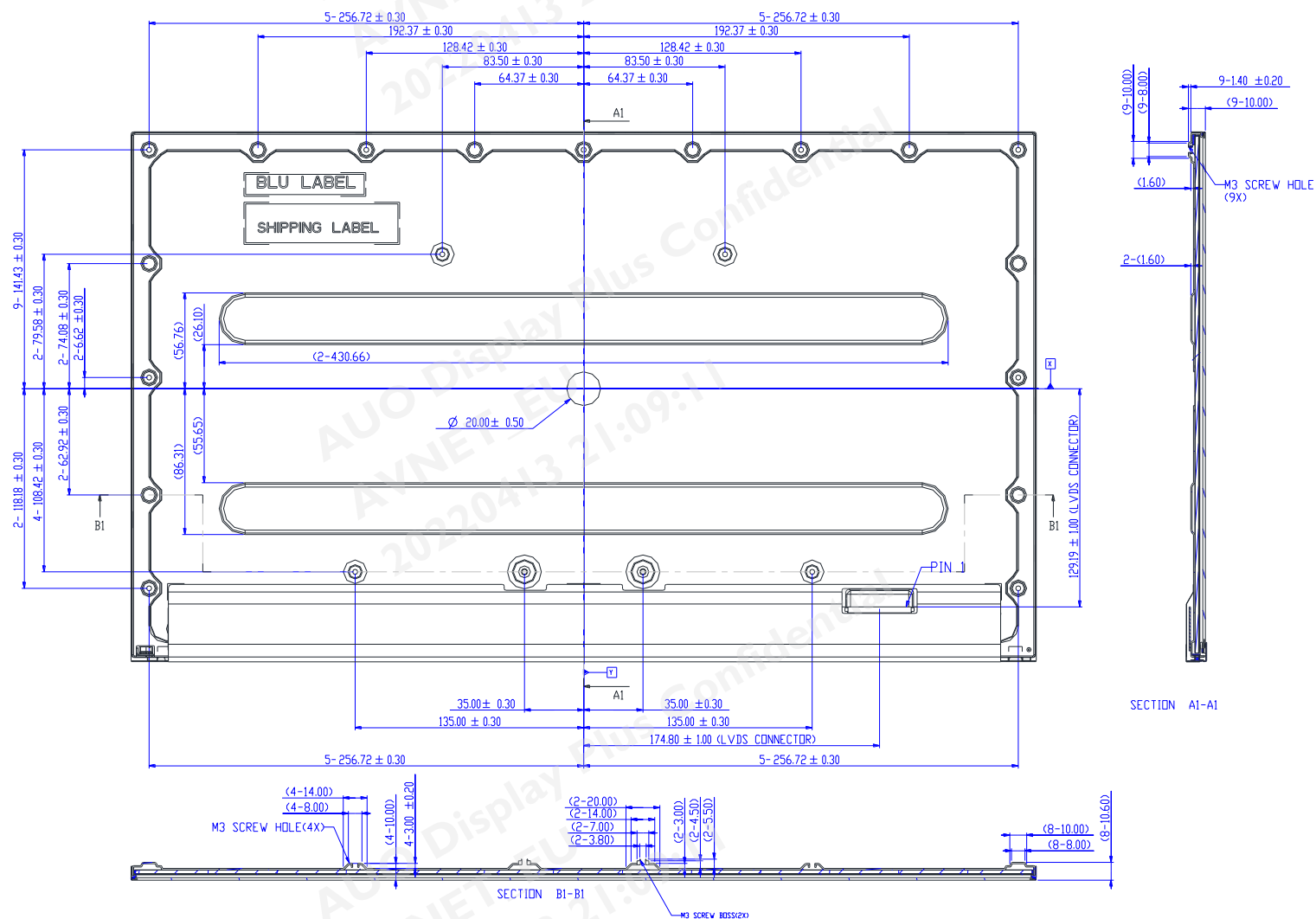
Note 6-4: The Green Mark will be presented only when the green documents have been ready by AUO Internal Green Team.

7 Preliminary Mechanical Characteristics

NOTES:

- 1.THE DIMENSION EXCLUDES DEFORMATION.
- 2.TOLERANCE WITHOUT NOTICED IS 0.5MM.
- 3.LIGHTBAR CONNECTOR IS CIVILUX CII406MIVL0-NH OR AUO APPROVED.
- 4.TORQUE OF M3 USER HOLE SHOULD BE WITHIN 6 kgf-cm MAX.
- 5.USER HOLE SCREW PENETRATION IS 3.0mm MAX.
- 6.MODULE THICKNESS IS 12.10±0.50mm (TO COVER FILM) & 13.20±0.50mm (TO LIGHTBAR CONNECTOR) & 13.30±0.50mm (TO 2PCS SCREW BOSS).
- 7.AVOID TOUCHING CDF IC POSITIONS WHEN DOING MECHANICAL DESIGN.

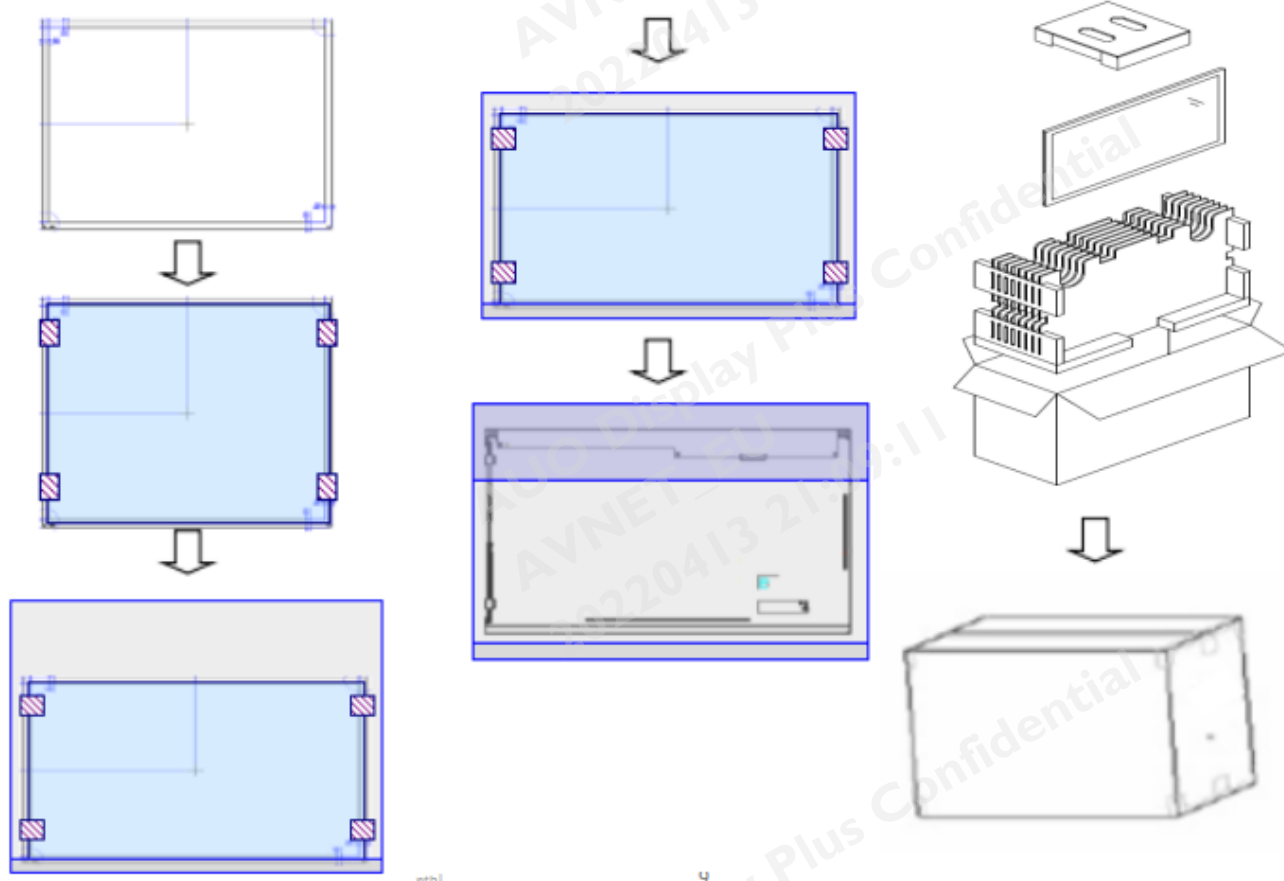




SECTION A1-A1

SECTION B1-B1

8.1 Packing Flow



Module by sea HQ : One pallet(1*4) *3 layers + One pallet (1*4) *2 layers, Total 140pcs module