

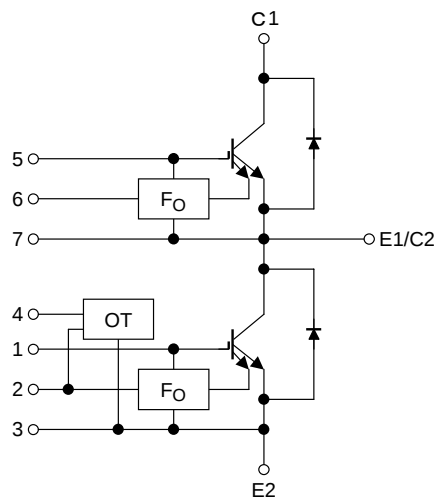
MG600J2YS61A(600V/600A 2in1)

High Power Switching Applications

Motor Control Applications

- Integrates a complete half bridge power circuit and fault-signal output circuit in one package.
(short circuit and over temperature)
- The electrodes are isolated from case.
- Low thermal resistance
- $V_{CE(sat)} = 2.2 \text{ V (typ.)}$

Equivalent Circuit



Signal terminal

1.	G (L)	2.	F _O (L)	3.	E (L)	4.	V _D
5.	G (H)	6.	F _O (H)	7.	E (H)	8.	Open

Maximum Ratings (Ta = 25°C)

Stage	Characteristics		Symbol	Rating	Unit
Inverter	Collector-emitter voltage		V_{CES}	600	V
	Gate-emitter voltage		V_{GES}	± 20	V
	Collector current	DC	I_C	600	A
		1 ms	I_{CP}	1200	
	Forward current	DC	I_F	600	A
		1 ms	I_{FM}	1200	
	Collector power dissipation (Tc = 25°C)		P_C	2770	W
Control	Control voltage (OT)		V_D	20	V
	Fault input voltage		V_{FO}	20	V
	Fault input current		I_{FO}	20	mA
Module	Junction temperature		T_j	150	°C
	Storage temperature range		T_{stg}	-40~125	°C
	Operation temperature range		T_{ope}	-20~100	°C
	Isolation voltage		V_{isol}	2500 (AC 1 min)	V
	Screw torque		—	3 (M5)	N·m

Electrical Characteristics (Tj = 25°C)**1. Inverter Stage**

Characteristics		Symbol	Test Condition		Min	Typ.	Max	Unit
Gate leakage current		I _{GES}	V _{GE} = ±20 V, V _{CE} = 0		—	—	+3/−4	mA
			V _{GE} = +10 V, V _{CE} = 0		—	—	100	nA
Collector cut-off current		I _{CES}	V _{CE} = 600 V, V _{GE} = 0		—	—	1.0	mA
Gate-emitter cut-off voltage		V _{GE (off)}	V _{CE} = 5 V, I _C = 600 mA		6.0	7.0	8.0	V
Collector-emitter saturation voltage		V _{CE (sat)}	V _{GE} = 15 V, I _C = 600 A	T _J = 25°C	—	2.2	2.5	V
				T _J = 125°C	—	—	2.8	
Input capacitance		C _{ies}	V _{CE} = 10 V, V _{GE} = 0, f = 1 MHz		—	125	—	nF
Switching time	Turn-on delay time	t _{d (on)}	V _{CC} = 300 V, I _C = 600 A V _{GE} = ±15 V, R _G = 5.1 Ω (Note 1)		0.10	—	1.00	μs
	Turn-off time	t _{off}			—	—	2.00	
	Fall time	t _f			—	—	0.50	
Reverse recovery time		t _{rr}			—	—	0.50	
Forward voltage		V _F	I _F = 600 A		—	2.2	2.6	V

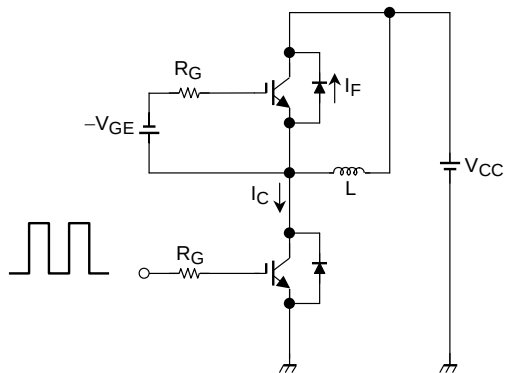
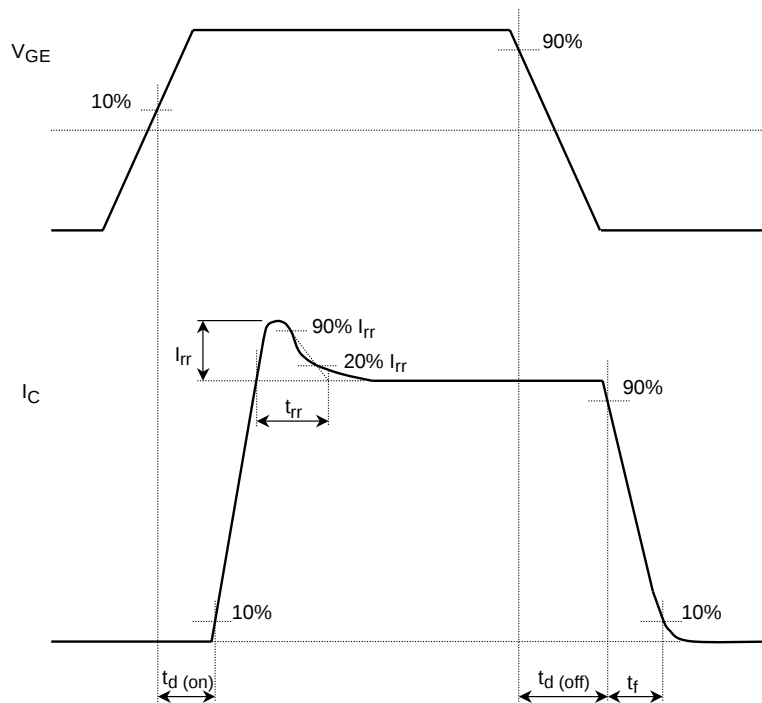
Note 1: Switching time test circuit & timing chart

2. Control (Tc = 25°C)

Characteristics	Symbol	Test Condition	Min	Typ.	Max	Unit
Fault output current	OC	$V_{GE} = 15 \text{ V}$	720	—	—	A
Over temperature	OT	—	100	—	125	°C
Fault output delay time	$t_d \text{ (Fo)}$	$V_{CC} = 300 \text{ V}, V_{GE} = \pm 15 \text{ V}$	—	—	6.5	μs

3. Module ($T_c = 25^\circ\text{C}$)

Characteristics	Symbol	Test Condition	Min	Typ.	Max	Unit
Junction to case thermal resistance	$R_{th(j-c)}$	Inverter IGBT stage	—	—	0.045	$^\circ\text{C/W}$
		Inverter FRD stage	—	—	0.068	
Case to fin thermal resistance	$R_{th(c-f)}$	With silicon compound	—	0.013	—	$^\circ\text{C/W}$

Switching Time Test Circuit**Timing Chart**

Remark**<Short circuit capability condition>**

- Short circuit capability is 6 μ s after fault output signal.
Please keep following condition to use fault output signal.
 - $V_{CC} \leq 375$ V
 - 13.8 V $\leq V_{GE} \leq 16.0$ V
 - $R_G \geq 5.1$ Ω
 - $T_j \leq 125^\circ$ C

<Gate voltage>

- To use this product, V_{GE} must be provided higher than 13.8 V.
In case V_{GE} is less than 13.8 V, fault signal FO may not be output even under error conditions.

<Recommended conditions for application>

Characteristics	Symbol	Min	Typ.	Max	Unit
P-N power terminal supply voltage	V_{CC}	—	300	375	V
Gate voltage	V_{GE}	13.8	15	16	V
Gate resistance	R_G	5.1	—	—	Ω
Switching frequency	fc	—	—	20	kHz

<For parallel use>

- For parallel use of this product, please use the same rank for both $V_{CE(sat)}$ and V_F among IGBT in parallel without fail.

 $V_{CE(sat)}$

$V_{CE(sat)}$	Min	Max
20	1.8	2.0
22	1.9	2.2
24	2.1	2.4
26	2.3	2.5

 V_F

V_F	Min	Max
D	1.9	2.2
E	2.1	2.4
F	2.3	2.6

