

SKM450GB12E4D1



SEMITRANS® 3

IGBT4 Modules

SKM450GB12E4D1

Features*

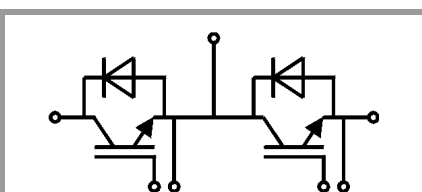
- IGBT4 = 4th generation medium fast trench IGBT (Infineon)
- CAL4 = Soft switching 4th generation CAL-diode
- Insulated copper baseplate using DBC technology (Direct Bonded Copper)
- Increased power cycling capability
- With integrated gate resistor
- For higher switching frequencies up to 12kHz
- UL recognized, file no. E63532
- SKM...D1: increased diode performance

Typical Applications

- AC inverter drives
- UPS

Remarks

- Case temperature limited to $T_c = 125^\circ\text{C}$ max.
- Recommended $T_{op} = -40 \dots +150^\circ\text{C}$
- Product reliability results valid for $T_j = 150^\circ\text{C}$



GB

Absolute Maximum Ratings				
Symbol	Conditions		Values	Unit
IGBT				
V _{CES}	T _j = 25 °C		1200	V
I _C	T _j = 175 °C	T _c = 25 °C	699	A
		T _c = 80 °C	538	A
I _{Cnom}			450	A
I _{CRM}	I _{CRM} = 3 x I _{Cnom}		1350	A
V _{GES}			-20 ... 20	V
t _{psc}	V _{CC} = 800 V V _{GE} ≤ 15 V V _{CES} ≤ 1200 V	T _j = 150 °C	10	μs
T _j			-40 ... 175	°C
Inverse diode				
V _{RRM}	T _j = 25 °C		1200	V
I _F	T _j = 175 °C	T _c = 25 °C	623	A
		T _c = 80 °C	466	A
I _{Fnom}			500	A
I _{FRM}	I _{FRM} = 2xI _{Fnom}		1000	A
I _{FSM}	t _p = 10 ms, sin 180°, T _j = 25 °C		2736	A
T _j			-40 ... 175	°C
Module				
I _{t(RMS)}			500	A
T _{stg}	module without TIM		-40 ... 125	°C
V _{isol}	AC sinus 50 Hz, t = 1 min		4000	V

Characteristics						
Symbol	Conditions		min.	typ.	max.	Unit
IGBT						
V _{CE(sat)}	I _C = 450 A	T _j = 25 °C		1.84	2.07	V
	V _{GE} = 15 V chiplevel	T _j = 150 °C		2.23	2.42	V
V _{CE0}	chiplevel	T _j = 25 °C		0.80	0.90	V
		T _j = 150 °C		0.70	0.80	V
r _{CE}	V _{GE} = 15 V chiplevel	T _j = 25 °C		2.3	2.6	mΩ
		T _j = 150 °C		3.4	3.6	mΩ
V _{GE(th)}	V _{GE} =V _{CE} , I _C = 16.4 mA		5	5.8	6.5	V
I _{CES}	V _{GE} = 0 V, V _{CE} = 1200 V, T _j = 25 °C				5	mA
C _{ies}	V _{CE} = 25 V V _{GE} = 0 V	f = 1 MHz		27.2		nF
C _{oes}		f = 1 MHz		1.76		nF
C _{res}		f = 1 MHz		1.50		nF
Q _G	V _{GE} = - 8 V...+ 15 V			2500		nC
R _{Gint}	T _j = 25 °C			1.9		Ω
t _{d(on)}	V _{CC} = 600 V	T _j = 150 °C		253		ns
t _r	I _C = 450 A	T _j = 150 °C		59		ns
E _{on}	V _{GE} = +15/-15 V	T _j = 150 °C		28		mJ
t _{d(off)}	R _{G on} = 1 Ω	T _j = 150 °C		505		ns
t _f	R _{G off} = 1 Ω	T _j = 150 °C		112		ns
E _{off}	di/dt _{on} = 8100 A/μs di/dt _{off} = 3400 A/μs	T _j = 150 °C		58		mJ
R _{th(j-c)}	per IGBT				0.062	K/W
R _{th(c-s)}	per IGBT (λ _{grease} =0.81 W/(m*K))			0.028		K/W
R _{th(c-s)}	per IGBT, pre-applied phase change material			0.017		K/W



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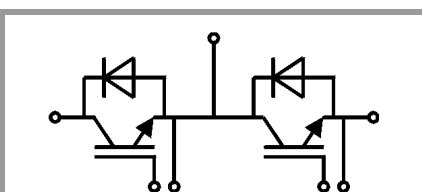
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Remarks

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Characteristics						
Symbol	Conditions		min.	typ.	max.	Unit
Inverse diode						
V _F = V _{EC}	I _F = 450 A	T _j = 25 °C		2.04	2.35	V
	V _{GE} = 0 V chipelevel	T _j = 150 °C		1.94	2.23	V
V _{F0}	chipelevel	T _j = 25 °C		1.30	1.50	V
		T _j = 150 °C		0.90	1.10	V
r _F	chipelevel	T _j = 25 °C		1.64	1.88	mΩ
		T _j = 150 °C		2.3	2.5	mΩ
I _{RRM}	I _F = 450 A	T _j = 150 °C		504		A
Q _{rr}	di/dt _{off} = 8000 A/μs	T _j = 150 °C		75		μC
E _{rr}	V _{GE} = -15 V V _{CC} = 600 V	T _j = 150 °C		31		mJ
R _{th(j-c)}	per diode				0.095	K/W
R _{th(c-s)}	per diode (λ _{grease} =0.81 W/(m*K))			0.037		K/W
R _{th(c-s)}	per diode, pre-applied phase change material			0.03		K/W
Module						
L _{CE}				15		nH
R _{CC'+EE'}	measured per switch	T _C = 25 °C		0.55		mΩ
		T _C = 125 °C		0.85		mΩ
R _{th(c-s)1}	calculated without thermal coupling			0.008		K/W
R _{th(c-s)2}	including thermal coupling, Ts underneath module (λ _{grease} =0.81 W/(m*K))			0.013		K/W
R _{th(c-s)2}	including thermal coupling, Ts underneath module, pre-applied phase change material			0.009		K/W
M _s	to heat sink M6		3		5	Nm
M _t		to terminals M6	2.5		5	Nm
						Nm
w					325	g



GB

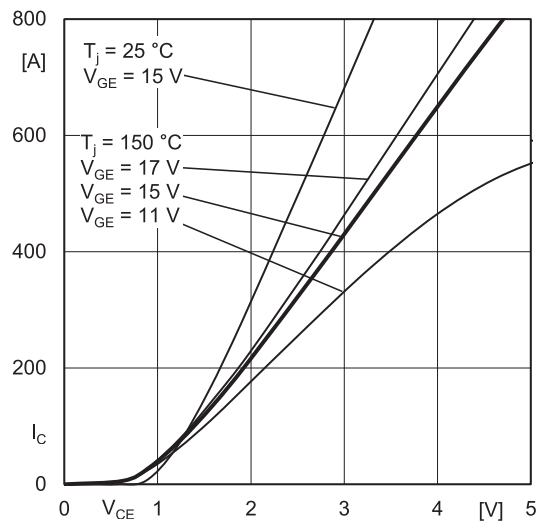


Fig. 1: Typ. output characteristic, inclusive $R_{CC'} + E_{E'}$

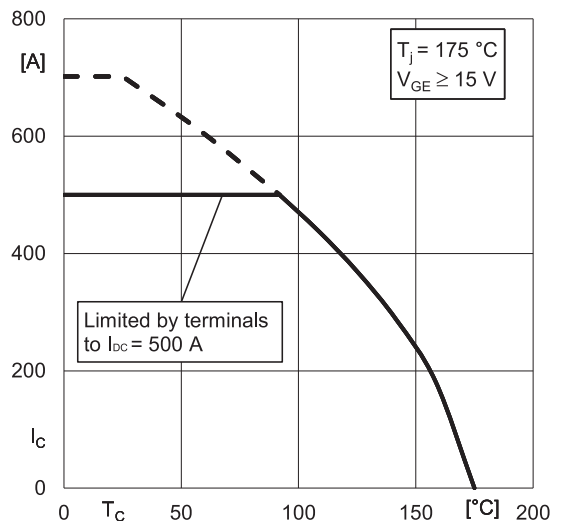


Fig. 2: Rated current vs. temperature $I_C = f(T_C)$

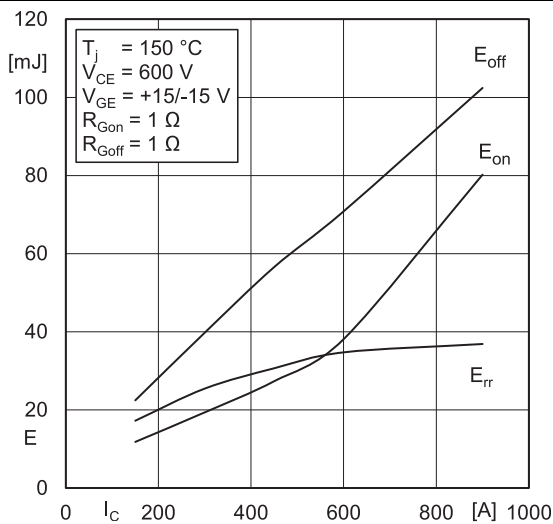


Fig. 3: Typ. turn-on /-off energy = $f(I_C)$

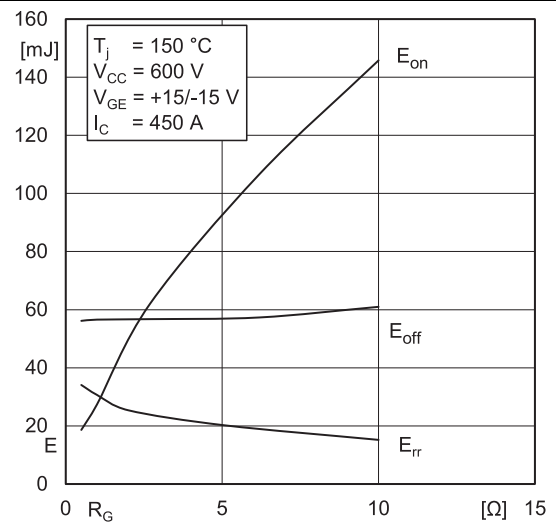


Fig. 4: Typ. turn-on /-off energy = $f(R_G)$

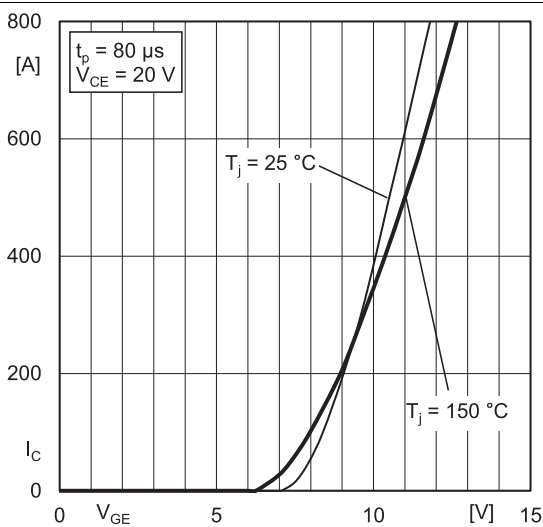


Fig. 5: Typ. transfer characteristic

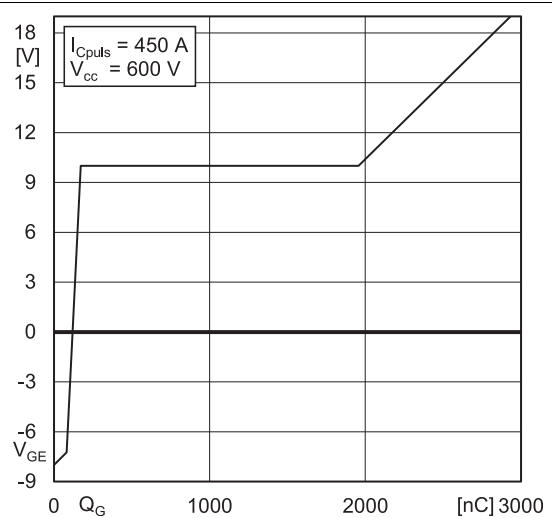


Fig. 6: Typ. gate charge characteristic

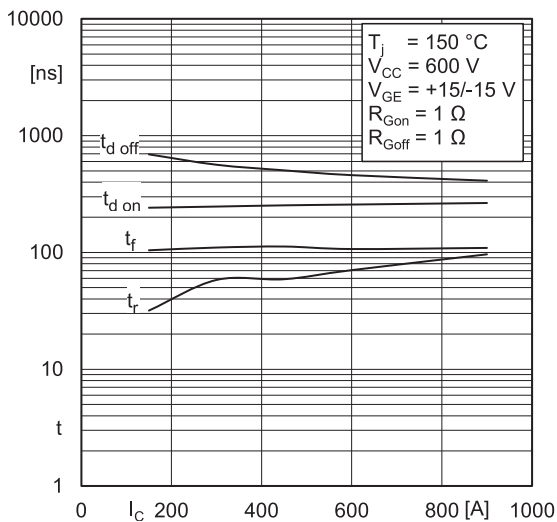


Fig. 7: Typ. switching times vs. I_C

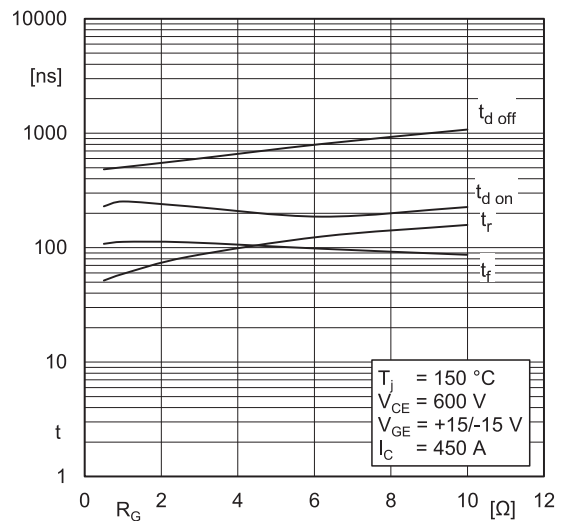


Fig. 8: Typ. switching times vs. gate resistor R_G

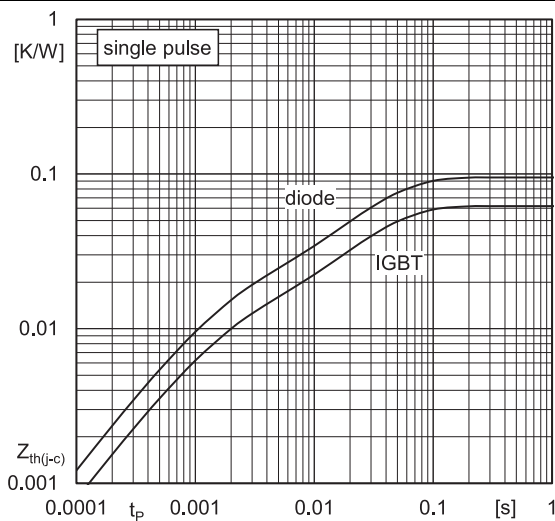


Fig. 9: Transient thermal impedance

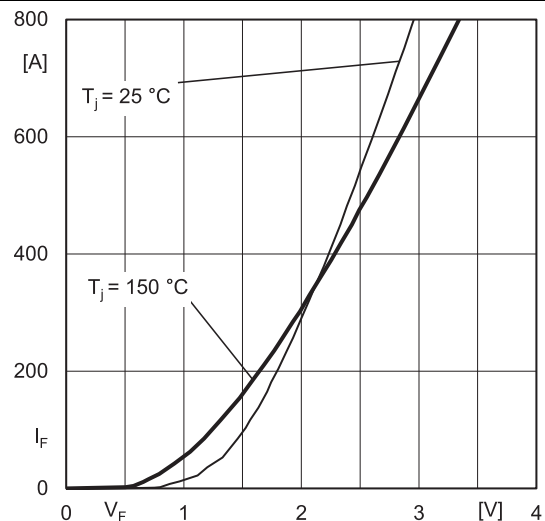


Fig. 10: Typ. CAL diode forward charact., incl. $R_{CC'}+EE'$

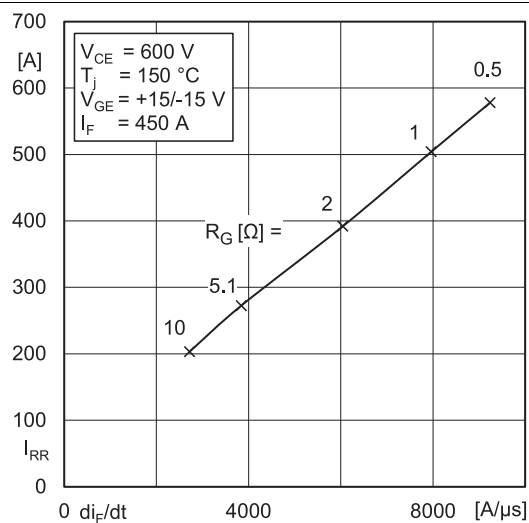


Fig. 11: Typ. CAL diode peak reverse recovery current

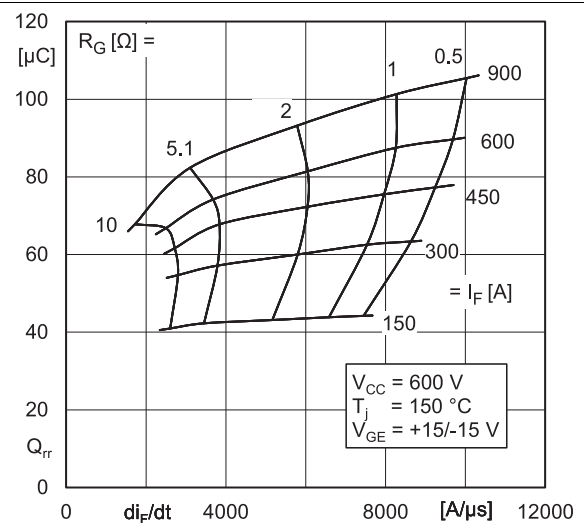
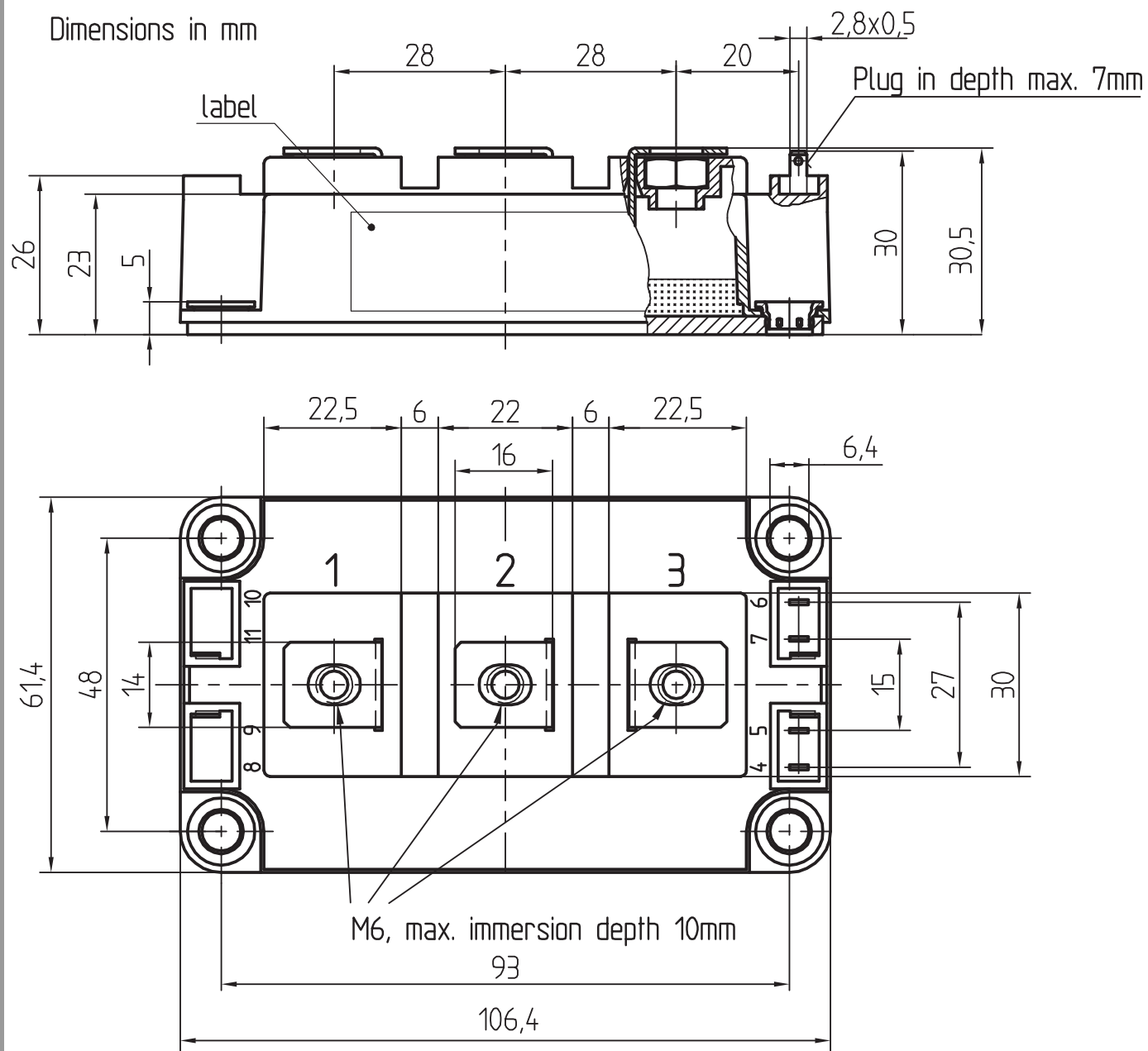
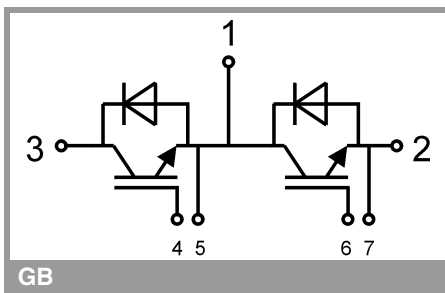


Fig. 12: Typ. CAL diode peak reverse recovery charge



General tolerance $\pm 0,5$ mm

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This is an electrostatic discharge sensitive device (ESDS), international standard IEC 60747-1, chapter IX.

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