

MODEL NO. : TM057KDH01-10ISSUED DATE: 2015/04/21VERSION : V1.0

- ☒ Preliminary Specification
☐ Final Product Specification

Customer : _____

Approved by	Notes

TIANMA Confirmed :

Prepared by	Checked by	Approved by

This technical specification is subjected to change without notice

Table of Contents

Record of Revision.....	3
1 General Specifications.....	4
2 Input/Output Terminals	5
3 Absolute Maximum Ratings.....	7
4 Electrical Characteristics	8
5 Timing Chart.....	9
6 Power Sequence.....	13
7 Optical Characteristics	14
8 Environmental / Reliability Test.....	17
9 Mechanical Drawing	18
10 Packing Drawing	19
11 Precautions for Use of LCD Modules.....	21

Record of Revision

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1. General Specifications

Feature		Spec
Display Spec.	Size	5.7 inch
	Resolution	320(RGB) X 240
	Technology Type	a-Si TFT
	Pixel Configuration	R.G.B Vertical Stripe
	Pixel pitch(mm)	0.360 (H) x 0.360(V)
	Display Mode	TM,NW
	Surface Treatment	Anti Glare
	Viewing Direction	6 o'clock
	Gray Scale Inversion Direction	12 o'clock (Source IC direction:12 o'clock)
Mechanical Characteristics	LCM (W x H x D) (mm)	144.0x104.6x12.3
	Active Area(mm)	115.2x86.4
	With /Without TSP	Without TSP
	Matching Connection Type	089H33-000100-G2-R (STARCONN)
	LED Numbers	15 LEDS
	Weight (g)	TBD
Electrical Characteristics	Interface	RGB 18 bits
	Color Depth	262K
	Driver IC	NT39413T*1+NT39208*1

Note 1: Viewing direction for best image quality is different from TFT definition. There is a 180 degree shift.

Note 2: Requirements on Environmental Protection: Q/S0002

Note 3: LCM weight tolerance: $\pm 5\%$

2. Input/Output Terminals

2.1 CN1 pin assignment (Signal interface)

Connector type: 089H33-000100-G2-R (STARCONN)

No	Symbol	I/O	Description	Comment
1	GND	P	Ground	
2	DOTCLK	I	Dot clock. Latch data at falling edge of DOTCLK.	
3	Hsync	I	Horizontal sync signal in SYNC mode. Pull low or floating in DE mode.	
4	Vsync	I	Vertical sync signal in SYNC mode. Pull low or floating in DE mode.	
5	GND	P	Ground	
6	R0	I	Red data (LSB)	
7	R1	I	Red data	
8	R2	I	Red data	
9	R3	I	Red data	
10	R4	I	Red data	
11	R5	I	Red data (MSB)	
12	GND	P	Ground	
13	G0	I	Green data(LSB)	
14	G1	I	Green data	
15	G2	I	Green data	
16	G3	I	Green data	
17	G4	I	Green data	
18	G5	I	Green data(MSB)	
19	GND	P	Ground	
20	B0	I	Blue data(LSB)	
21	B1	I	Blue data	
22	B2	I	Blue data	
23	B3	I	Blue data	
24	B4	I	Blue data	
25	B5	I	Blue data(MSB)	
26	GND	P	Ground	
27	ENABLE	I	Data enable signal in DE mode. This pin must be floating when SYNC mode.	
28	VCC	P	Power supply	
29	VCC	P	Power supply	
30	R/L	I	Set horizontal scan direction: Low/NC:left to right; High:right to left	

31	U/D	I	Set vertical scan direction: High/NC:up to down; Low:down to up	
32	NC	-	No connection	
33	GND	P	Ground	

P: Power/GND; I: input pin;

Note1: CN1 Matching FPC type: 33 pin, pitch: 0.5mm, height: 0.3mm.

Table 3.1 input terminal pin assignments

2.2 CN2 pin assignment (Backlight interface)

Connector type: SHLP-06V-S-B (JST)

No	Symbol	I/O	Description	Remark
1	AN1	P	LED driving anode 1 (high voltage)	
2	AN2	P	LED driving anode 2 (high voltage)	
3	AN3	P	LED driving anode 3 (high voltage)	
4	CA1	P	LED driving cathode 1 (low voltage)	
5	CA2	P	LED driving cathode 2 (low voltage)	
6	CA3	P	LED driving cathode 3 (low voltage)	

Note1: CN2 Matching Connector type: SM06B-SHLS-TF (JST)

Table2.2 Backlight terminal pin assignments

3. Absolute Maximum Ratings

GND=0V, Ta = 25°C

Item	Symbol	MIN	MAX	Unit	Remark
Power Voltage	VCC	-0.5	5.0	V	
	AVDD	-0.5	15.0	V	
	VGG	-0.3	40.0	V	
	VEE	-20.0	0.3	V	
	VGG-VEE	-0.3	40.0	V	
Input voltage	V _{IN}	-0.5	5.0	V	Note2
Operating Temperature	Top	-20	85	°C	Note1
Storage Temperature	Tst	-55	125	°C	Note1

Note1: The parameter is for driver IC (gate driver, source driver) only.

Note2: Signals include R0~R5, G0~G5, B0~B5, Dotclk, Hsync, Vsync, Enable, R/L, U/D.

Table 3.1 absolute maximum rating

4. Electrical Characteristics

4.1 LCD DC Characteristics

GND=0V, Ta=25°C

Item	Symbol	MIN	TYP	MAX	Unit	Remark
Supply Voltage	VCC	3.00	3.30	3.60	V	
Input Signal Voltage	Low Level	V_{IL}	0	-	$0.3 \times VCC$	V
	High Level	V_{IH}	$0.7 \times VCC$	-	VCC	V
Output Signal Voltage	Low Level	V_{OL}	GND	-	$GND + 0.4$	V
	High Level	V_{OH}	$VCC - 0.4$	-	-	V
Common Electrode Driving Signal	VCOM	-	4.87	-	V	Note1
Current of VCC Power supply	I_{VCC}	-	TBD	-	mA	Note2

Note1: For different LCM, the value may have a bit of difference.

Note2: To test the current dissipation, use "all Black Pattern".

Table 4.1 LCD module electrical characteristics

4.2 Backlight Unit Driving Condition

Item	Symbol	MIN	TYP	MAX	Unit	Remark
Channel 1 Forward Current	$I_{channel\ 1}$	-	25.0	-	mA	
Channel 2 Forward Current	$I_{channel\ 2}$	-	25.0	-	mA	
Channel 3 Forward Current	$I_{channel\ 3}$	-	25.0	-	mA	
Forward Voltage	V_{BL}	14.85	-	18.15	V	Note 2
Backlight Power Consumption	W_{BL}	-	1238	-	mW	Note 1

Note 1: The LED driving condition is defined for total backlight consumption.

Note 2: Forward Voltage adjusting must depend on Forward Current setting.

Note 3: One LED: $I_F = 25\text{ mA}$, $V_F = 3.3\text{V}$.

Table 4.2 LED backlight characteristics

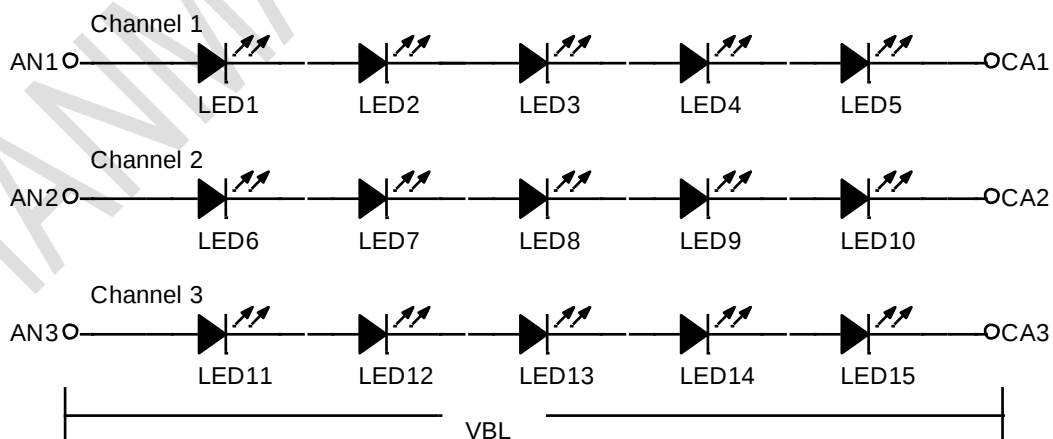


Figure 4.2 LED connection of backlight

5. Timing Chart

5.1 TCON (Embedded In Source IC) Input Timing

Parameter	Symbol	Symbol	Min.	Typ.	Max.	Unit
Dotclk	Dotclk frequency	Fclk	6.2	6.4	12.1	MHz
	Dotclk cycle	Tclk	82.64	156.25	161.29	ns
Hsync	Horizontal display area	Thd	320	320	320	Tclk
	1 horizontal line	Th	406	408	560	Tclk
	Hsync pulse width	Thpw	1	-	-	Tclk
	Horizontal blank	Thb	70	70	70	Tclk
	Horizontal front porch	Thfp	16	18	170	Tclk
	Vertical display area	Tvd	240	240	240	Th
	Vsync period time	Tv	254	263	360	Th
Vsync	Vsync pulse width	Tvpw	1	-	-	Th
	Vsync blank	Tvb	13	13	13	Th
	Vsync front porch	Tvfp	1	10	107	Th

Note: DE timing refer to Hsync, Vsync input timing.

Table 5.1 Input Timing Control Conditions

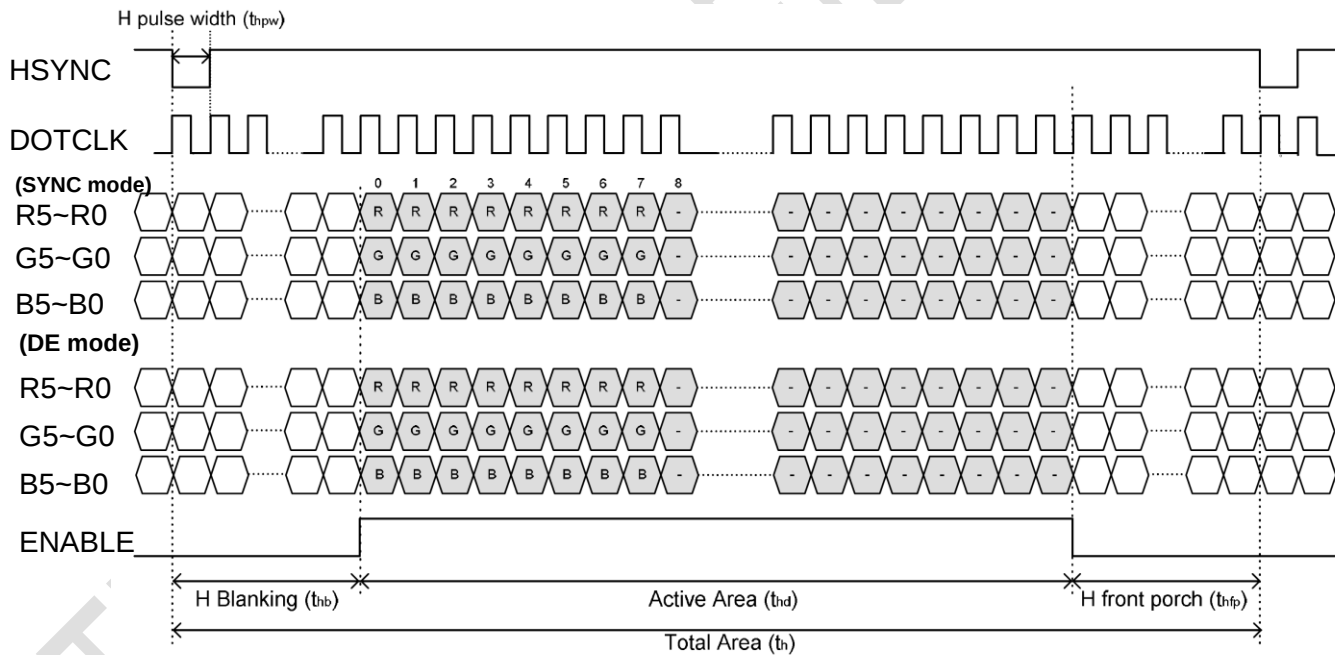


Figure 5.1.1 RGB interface input timing

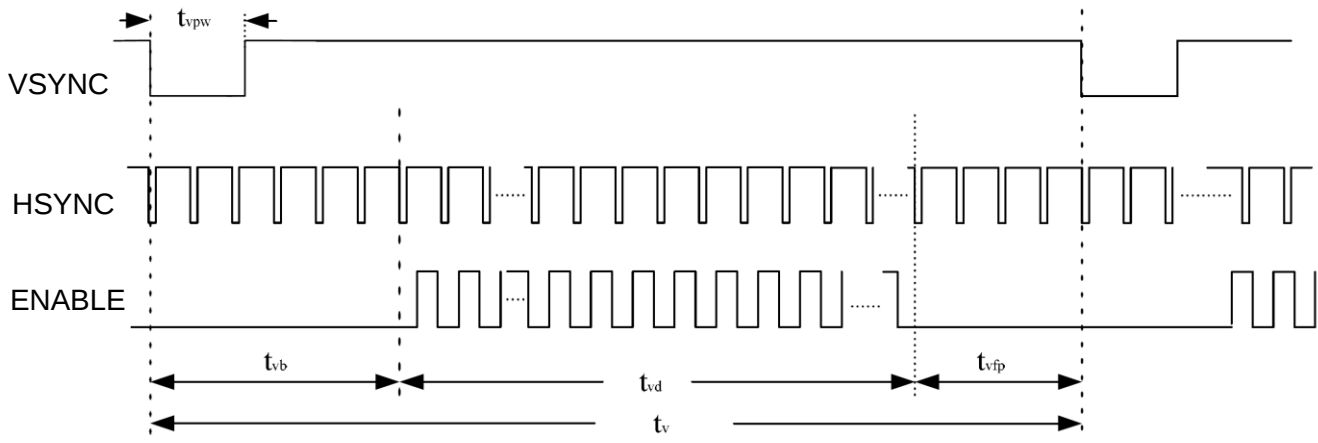


Figure 5.1.2 RGB interface input timing (continued)

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5.2 AC input characteristics

(VCC=3.3V, AVDD=10.65V, AVSS=GND=0V, Ta=25°C)

Parameter	Symbol	Min	Typ	Max	Unit	Conditions
DOTCLK pulse duty	Tcwh	40%	50%	60%	Tclk	Tcph is DCLK cycle
VSYNC setup time	Tvst	10	-	-	ns	
VSYNC hold time	Tvhd	10	-	-	ns	
HSYNC setup time	Thst	10	-	-	ns	
HSYNC hold time	Thhd	10	-	-	ns	
Data setup time	Tdsu	10	-	-	ns	Rn, Gn, Bn to DCLK
Data hold time	Tdhd	10	-	-	ns	Rn, Gn, Bn to DCLK
Enable setup time	Tesu	10			ns	
Enable hold time	Tehd	10			ns	

Table 5.2 AC input characteristics

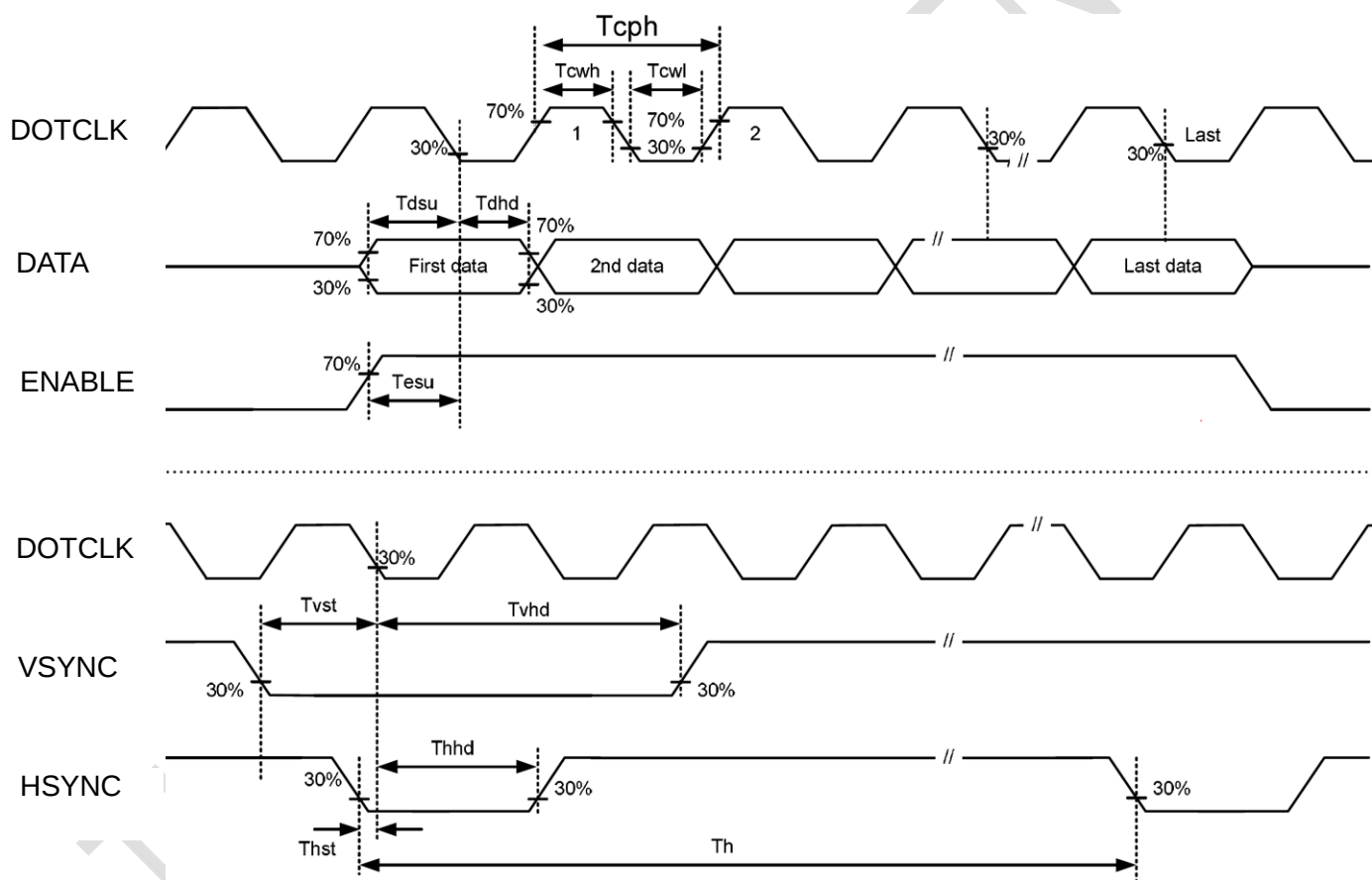


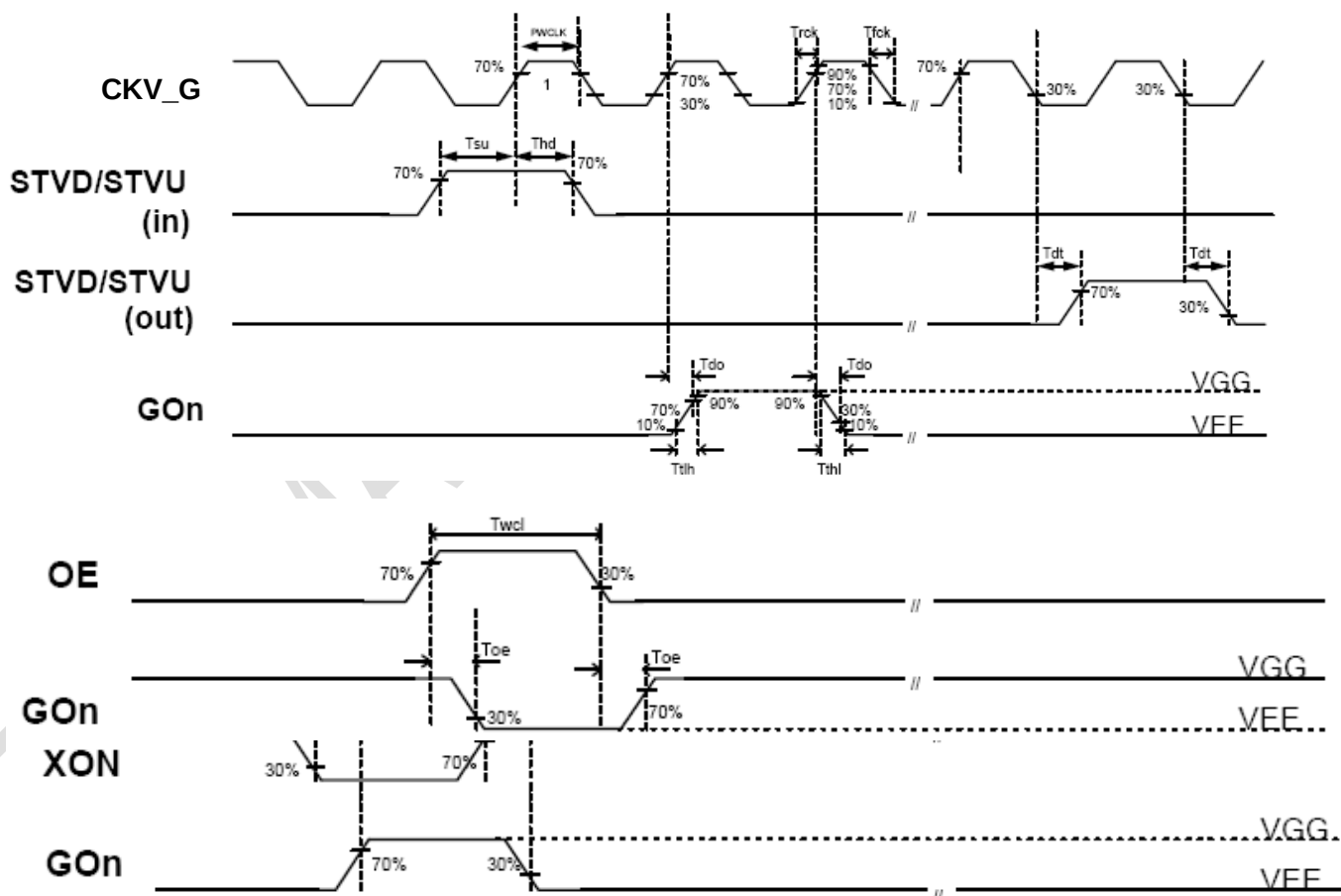
Figure 5.2 AC input characteristics

5.3 Gate driver input timing

(VGG=20V, VEE=-7V, VCC_G=3.3V, GND=0V, Ta=25°C)

Symbol	Parameters	Min	Typ	Max	Unit	Conditions
Tdt	STVD/STVU delay time	-	-	500	ns	CL=20pF
Tdo	Driver output delay time	-	-	900	ns	CL=200pF When Twcl=1us
Tthl	Output falling time	-	400	800	ns	CL=200pF 90% to 10%
Ttlh	Output rising time	-	500	1000	ns	CL=200pF 10% to 90%
Txon	XON to driver output delay time	-	-	50	us	CL=200pF
Toe	OEx to driver output delay time	-	-	900	ns	CL=200pF
F _{CKV_G}	Gate clock frequency	-	-	200	KHz	In cascade connection
Trck	Gate clock rising time	-	-	100	ns	CL=20pF
Tfck	Gate clock falling time	-	-	100	ns	CL=20pF
PWCLK	Clock pulse width (High & Low)	500	-	-	ns	
Tsu	STVD/STVU set-up time	200	-	-	ns	
Thd	STVD/STVU hold time	300	-	-	ns	
Twcl	Output enabled pulse width	1	-	-	us	

Table 5.3 Gate driver input timing



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6. POWER ON/OFF SEQUENCE

Item	Symbol	MIN	TYP	MAX	Unit	Remark
VCC 3.0V to signal starting	Tp1	5	-	50	ms	
Signal starting to backlight on	Tp2	50	-	-	ms	
Signal off to VCC 3.0V	Tp3	0	-	50	ms	
Backlight off to signal off	Tp4	50	-	-	ms	

Table 6.0 Power on/off sequence

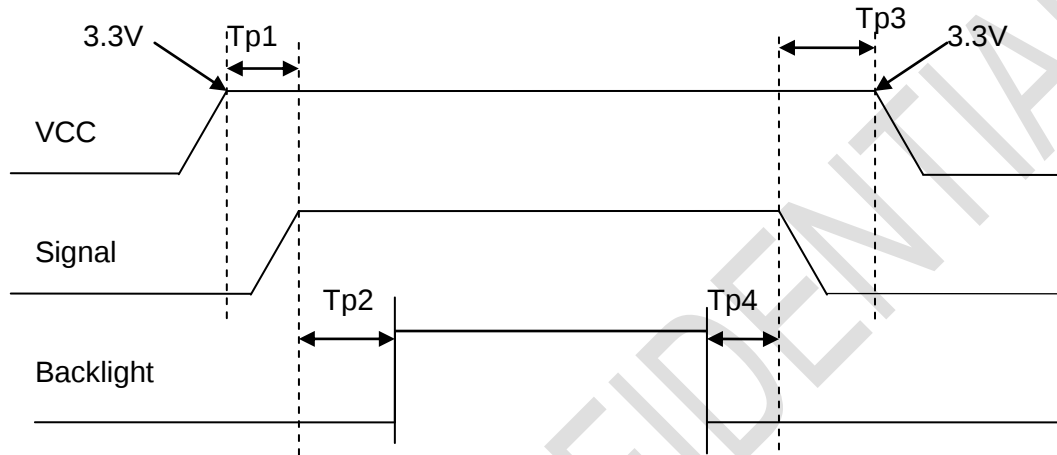


Figure 6.0 Power on/off sequence

7. Optical Characteristics

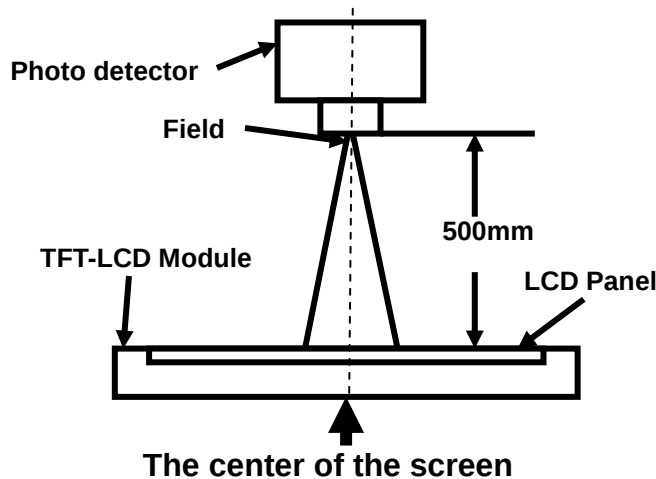
Item		Symbol	Condition	Min	Typ	Max	Unit	Remark
View Angles		θT	CR ≧ 10	60	70	-	Degree	Note2,3
		θB		50	60	-		
		θL		60	70	-		
		θR		60	70	-		
Contrast Ratio		CR	θ=0°	400	500	-		Note 3
Response Time		T _{ON}	25℃	-	20	30	ms	Note 4
		T _{OFF}						
Chromaticity	White	x	Backlight is on	0.251	0.301	0.351		Note 1,5
		y		0.282	0.332	0.382		
	Red	x		0.558	0.608	0.658		Note 1,5
		y		0.297	0.347	0.397		
	Green	x		0.279	0.329	0.379		Note 1,5
		y		0.51	0.56	0.61		
	Blue	x		0.089	0.139	0.189		Note 1,5
		y		0.065	0.115	0.165		
Uniformity		U		75	80	-	%	Note 6
NTSC				45	50	-	%	Note 5
Luminance		L		350	400	-	cd/m ²	Note 7

Test Conditions:

1. $I_F = 180$ mA, and the ambient temperature is 25℃.
2. The test systems refer to Note 1 and Note 2.

Note 1: Definition of optical measurement system.

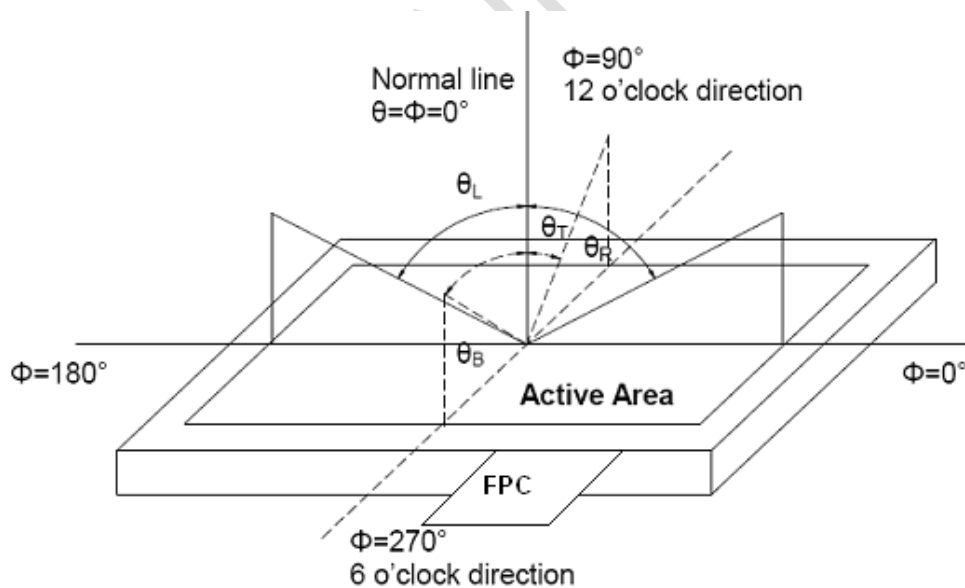
The optical characteristics should be measured in dark room. After 5 Minutes operation, the optical properties are measured at the center point of the LCD screen. All input terminals LCD panel must be ground when measuring the center area of the panel.



Item	Photo detector	Field
Contrast Ratio	SR-3A	1°
Luminance		
Chromaticity		
Lum Uniformity		
Response Time	BM-7A	2°

Note 2: Definition of viewing angle range and measurement system.

viewing angle is measured at the center point of the LCD by CONOSCOPE(ergo-80).



Note 3: Definition of contrast ratio

$$\text{Contrast ratio (CR)} = \frac{\text{Luminance measured when LCD is on the "White" state}}{\text{Luminance measured when LCD is on the "Black" state}}$$

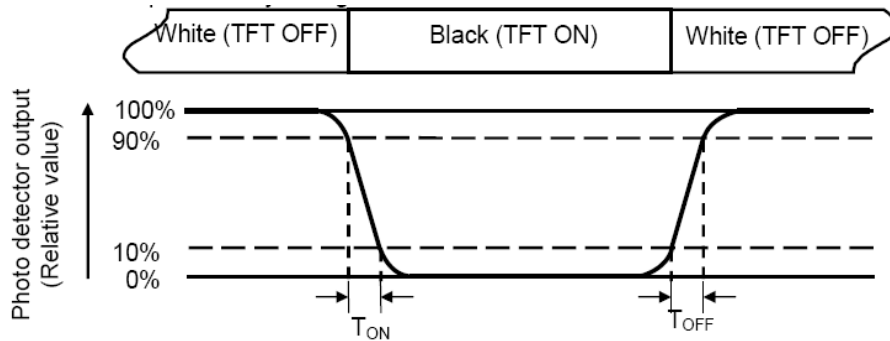
“White state “: The state is that the LCD should drive by Vwhite.

“Black state”: The state is that the LCD should drive by Vblack.

V_{white}: To be determined V_{black}: To be determined.

Note 4: Definition of Response time

The response time is defined as the LCD optical switching time interval between “White” state and “Black” state. Rise time (T_{ON}) is the time between photo detector output intensity changed from 90% to 10%. And fall time (T_{OFF}) is the time between photo detector output intensity changed from 10% to 90%.



Note 5: Definition of color chromaticity (CIE1931)

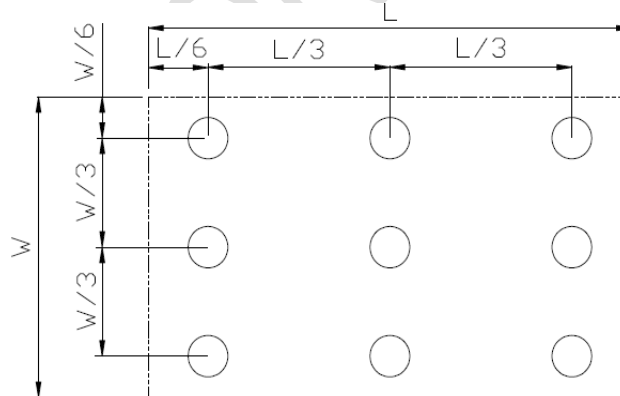
Color coordinates measured at center point of LCD.

Note 6: Definition of Luminance Uniformity

Active area is divided into 9 measuring areas (Refer Fig. 2). Every measuring point is placed at the center of each measuring area.

$$\text{Luminance Uniformity (U)} = L_{\min} / L_{\max}$$

L-----Active area length W----- Active area width



L_{max}: The measured Maximum luminance of all measurement position.

L_{min}: The measured Minimum luminance of all measurement position.

Note 7: Definition of Luminance:

Measure the luminance of white state at center point.

8. Environmental / Reliability Test

No	Test Item	Condition	Remarks
1	High Temperature Operation	Ts = +70℃, 240 hours	IEC60068-2-1:2007 GB2423.2-2008
2	Low Temperature Operation	Ta = -20℃, 240 hours	IEC60068-2-1:2007 GB2423.1-2008
3	High Temperature Storage	Ta = +80℃, 240 hours	IEC60068-2-1:2007 GB2423.2-2008
4	Low Temperature Storage	Ta = -30℃, 240 hours	IEC60068-2-1:2007 GB2423.1-2008
5	Storage at High Temperature and Humidity	Ta = +60℃, 90% RH max, 240 hours	IEC60068-2-78 :2001 GB/T2423.3—2006
6	Thermal Shock (non-operation)	-30℃ 30 min~+80℃ 30 min, Change time:5min, 100 Cycle	Start with cold temperature, End with high temperature, IEC60068-2-14:1984,G B2423.22-2002
7	ESD	C=150pF,R=330Ω,5point/panel Air:±8Kv,5times; Contact:±4Kv,5times (Environment:15℃~35℃, 30%~60%.86Kpa~106Kpa)	IEC61000-4-2:2001 GB/T17626.2-2006
8	Vibration Test	Frequency range:10~55Hz Stroke:1.5mm Sweep:10Hz~55Hz~10Hz 2 hours for each direction of X.Y.Z. (6 hours for total)	IEC60068-2-6:1982 GB/T2423.10—1995
9	Mechanical Shock (Non OP)	Half Sine Wave 50G 17ms, ±X,±Y,±Z 3times for each direction	IEC60068-2-27:1987 GB/T2423.5—1995
10	Package Drop Test	Height:80cm, 1corner,3edges,6surfaces	IEC60068-2-32:1990 GB/T2423.8—1995

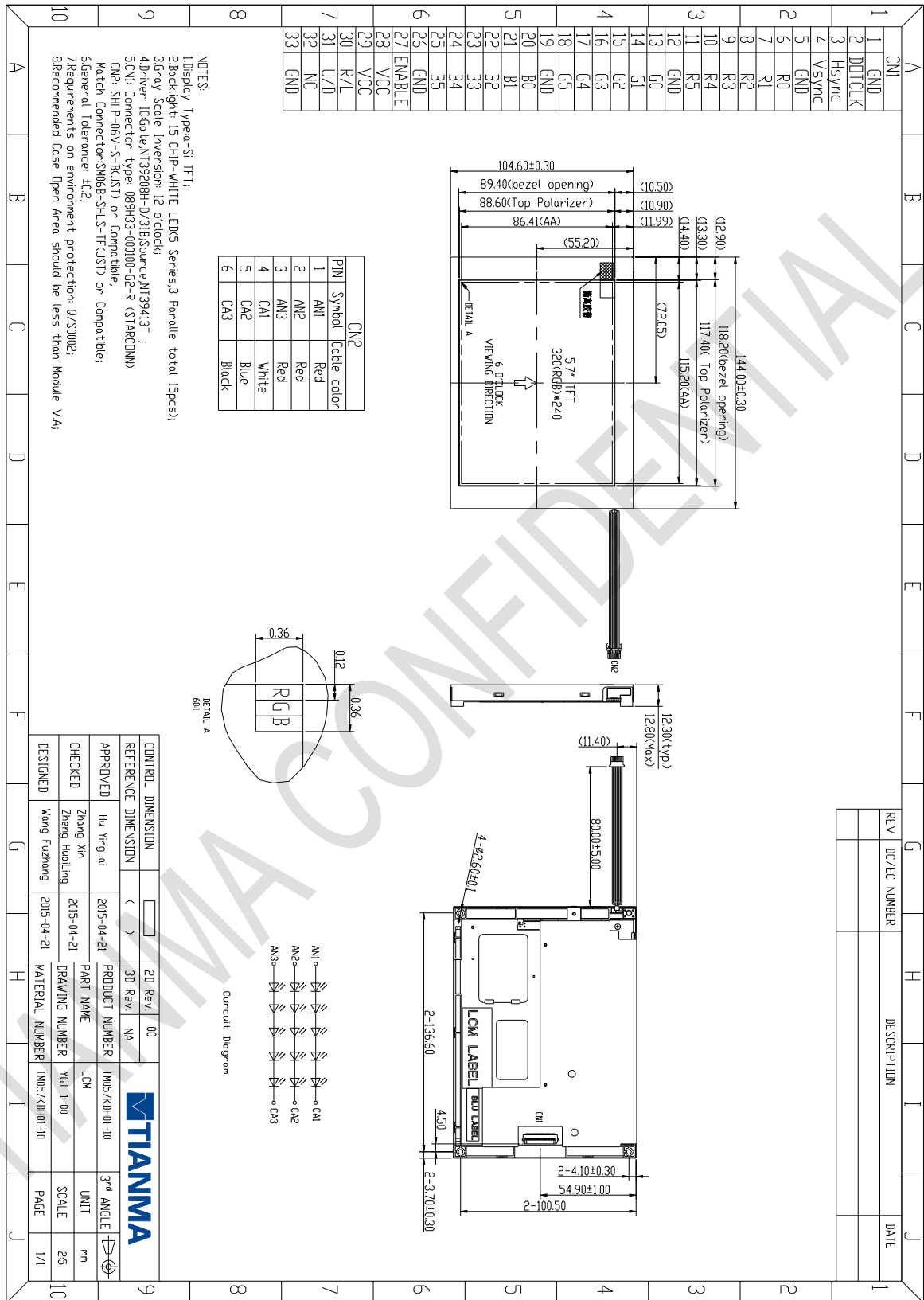
Note1: Ts is the temperature of panel's surface.

Note2: Ta is the ambient temperature of sample.

Note3: Before cosmetic and function test, the product must have enough recovery time, at least 2 hours at room temperature.

Note 4: In the standard condition, there shall be no practical problem that may affect the display function. After the reliability test, the product only guarantees operation, but don't guarantee all of the cosmetic specification.

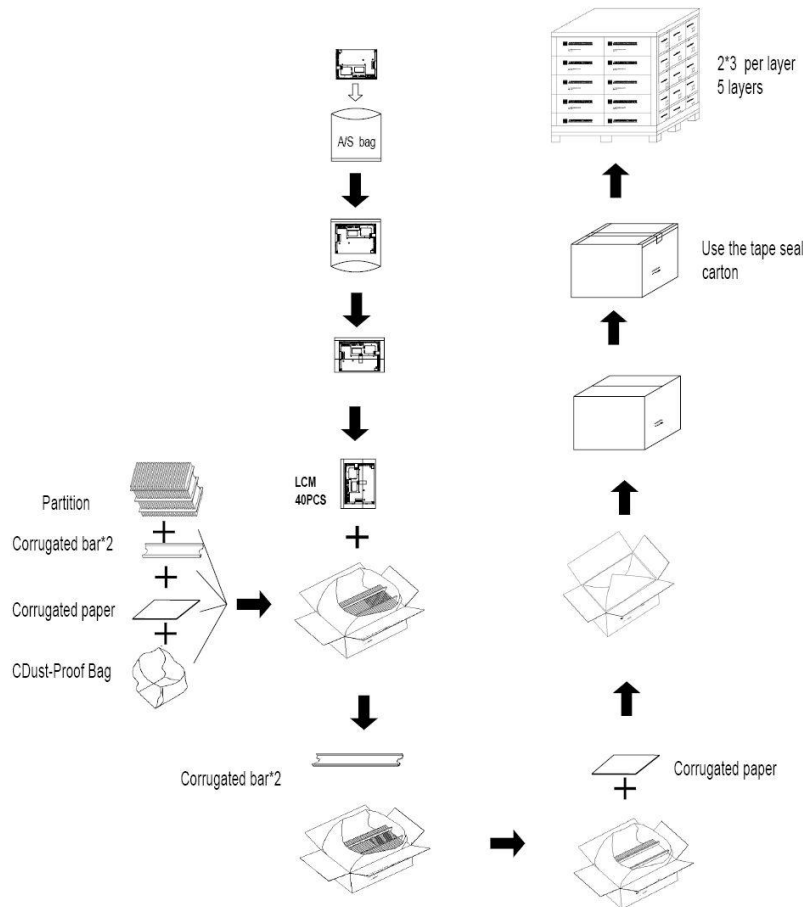
9. Mechanical Drawing



10. Packing Drawing

Per Carton

No	Item	Model (Material)	Dimensions(mm)	Unit Weight(Kg)	Quantity	Remark
1	LCM module	TM057KDH01-10	144X104.6X12.3	0.16	40	
2	Partition_1	Corrugated paper	513X333X215	1.388	1	
3	Anti-static Bag	PE	180X165X0.05	0.001	40	Anti-static
4	Dust-Proof Bag	PE	700X530	0.06	1	
5	Partition_2	Corrugated Paper	505.0X332.0X4.0	0.098	2	
6	Corrugated Bar	Corrugated paper	513X110×31.0	0.048	4	
7	Carton	Corrugated paper	530X350X250	1.12	1	
8	Total weight	9.5KG				



11. Precautions for Use of LCD Modules

11.1 Handling Precautions

11.1.1 The display panel is made of glass. Do not subject it to a mechanical shock by dropping it from a high place, etc.

11.1.2 If the display panel is damaged and the liquid crystal substance inside it leaks out, be sure not to get any in your mouth, if the substance comes into contact with your skin or clothes, promptly wash it off using soap and water.

11.1.3 Do not apply excessive force to the display surface or the adjoining areas since this may cause the color tone to vary.

11.1.4 The polarizer covering the display surface of the LCD module is soft and easily scratched. Handle this polarizer carefully.

11.1.5 If the display surface is contaminated, breathe on the surface and gently wipe it with a soft dry cloth. If still not completely clear, moisten cloth with one of the following solvents:

- Isopropyl alcohol
- Ethyl alcohol

Solvents other than those mentioned above may damage the polarizer. Especially, do not use the following:

- Water

— Ketone

— Aromatic solvents

11.1.6 Do not attempt to disassemble the LCD Module.

11.1.7 If the logic circuit power is off, do not apply the input signals.

11.1.8 To prevent destruction of the elements by static electricity, be careful to maintain an optimum work environment.

11.1.8.1 Be sure to ground the body when handling the LCD Modules.

11.1.8.2 Tools required for assembly, such as soldering irons, must be properly ground.

11.1.8.3 To reduce the amount of static electricity generated, do not conduct assembly and other work under dry conditions.

11.1.8.4 The LCD Module is coated with a film to protect the display surface. Be care when peeling off this protective film since static electricity may be generated.

11.2 Storage precautions

11.2.1 When storing the LCD modules, avoid exposure to direct sunlight or to the light of fluorescent lamps.

11.2.2 The LCD modules should be stored under the storage temperature range. If the LCD modules will be stored for a long time, the recommend condition is:

Temperature : 0℃ ~ 40℃ Relatively humidity: ≤80%

11.2.3 The LCD modules should be stored in the room without acid, alkali and harmful gas.

11.3 Transportation Precautions

11.3.1 The LCD modules should be no falling and violent shocking during transportation, and also should avoid excessive press, water, damp and sunshine.